Novel Memory Models for Symbolic Execution

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by David Trabish
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Symbolic Execution

Program analysis technique

- Systematically explores paths
- Checks feasibility using SMT

Applications:

- Test case generation
- Bug finding
- …

Today's Talk

Challenges	Path explosion Constraint solving False negatives
Our Attack	Novel memory models

Outline

- Background
  - Symbolic execution
  - Memory model
- Symbolic base addresses
  - Relocatable memory model
  - Address-aware query caching
- Symbolic-size allocations
  - Bounded symbolic-size model
  - State merging with quantifiers
- Conclusions and future work

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Symbolic Execution: Example

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int get_sign(int x) {
  if (x == 0) {
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  if (x < 0) {
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  } else {
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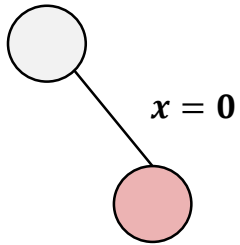

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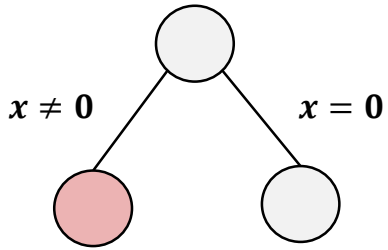

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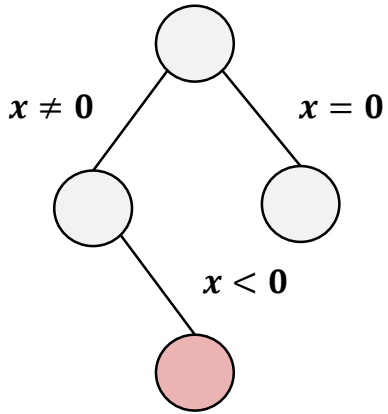

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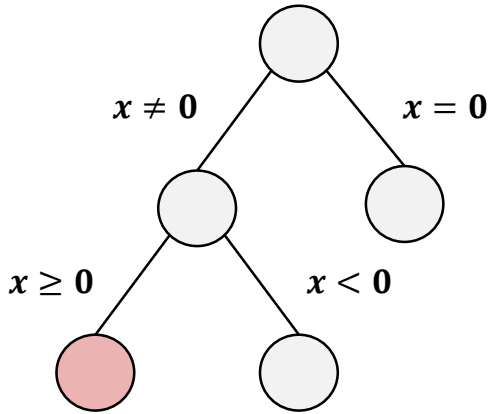

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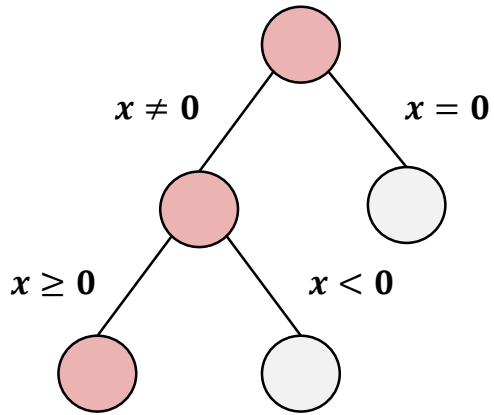

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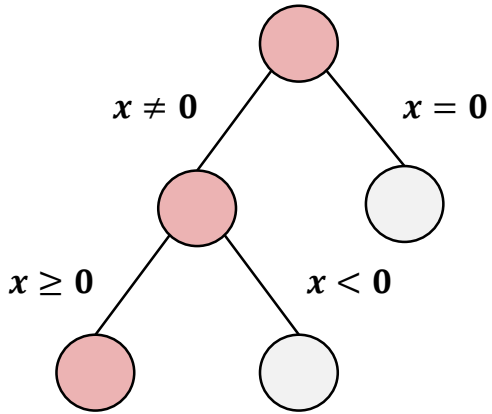

$x \neq 0 \wedge x \geq 0$

SMT

$x \mapsto 7$

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Standard Memory Model

Two main components:

- Memory objects
  - Integers, arrays, heap allocations, etc.
- Address space
  - Location of memory objects

Memory Objects

Defined by a tuple (b, s, a):

- Concrete base address
- Concrete size
- SMT array

Memory Objects

Reading at offset i from (b, s, a):

$$(i < 0 \ \vee \ i \geq s) \xrightarrow{SAT} \text{out-of-bounds error}$$

Memory Objects

Reading at offset i from (b, s, a):

$$(i < 0 \ \vee \ i \geq s) \xrightarrow{\textit{UNSAT}} \underbrace{select(a, i)}_{\text{read value}}$$

Memory Objects

Writing v at offset i in (b, s, a):

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Memory Objects

Writing v at offset i in (b, s, a):

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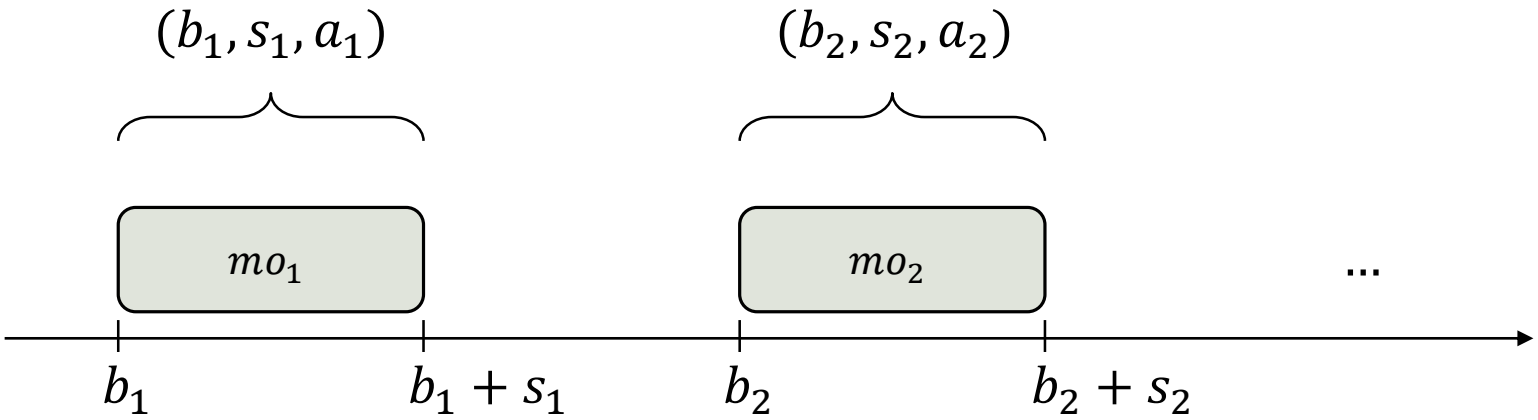
Address Space

- Linear space
- Disjoint intervals



Memory Operations

- Allocation
- Dereference
- Deallocation

Memory Operations

Allocate n bytes

Memory Operations

Allocate n bytes

concretize n to s

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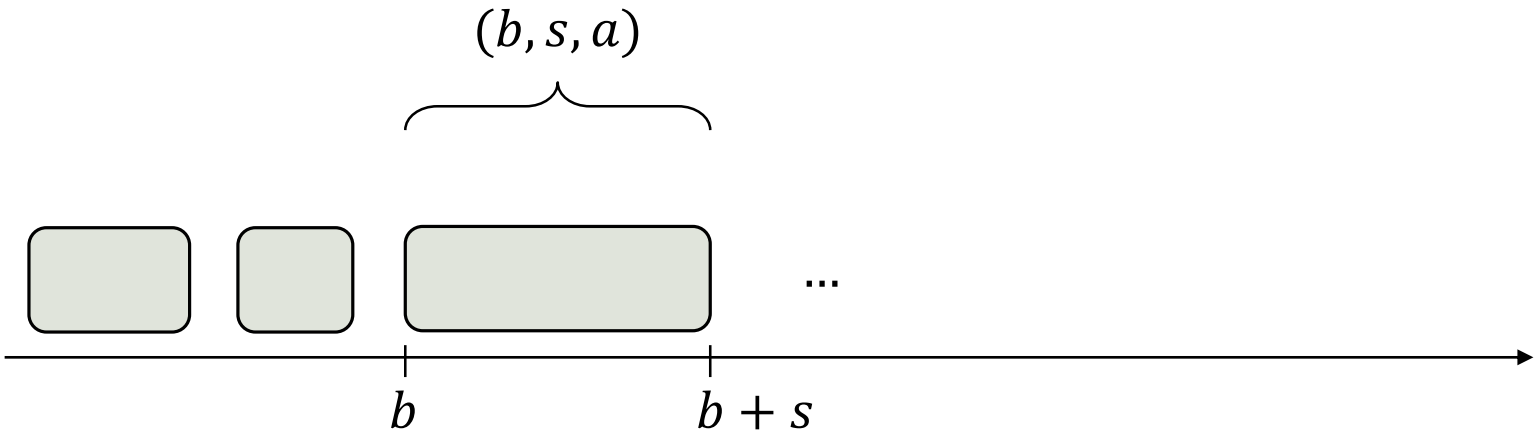
concretize n to s



Memory Operations

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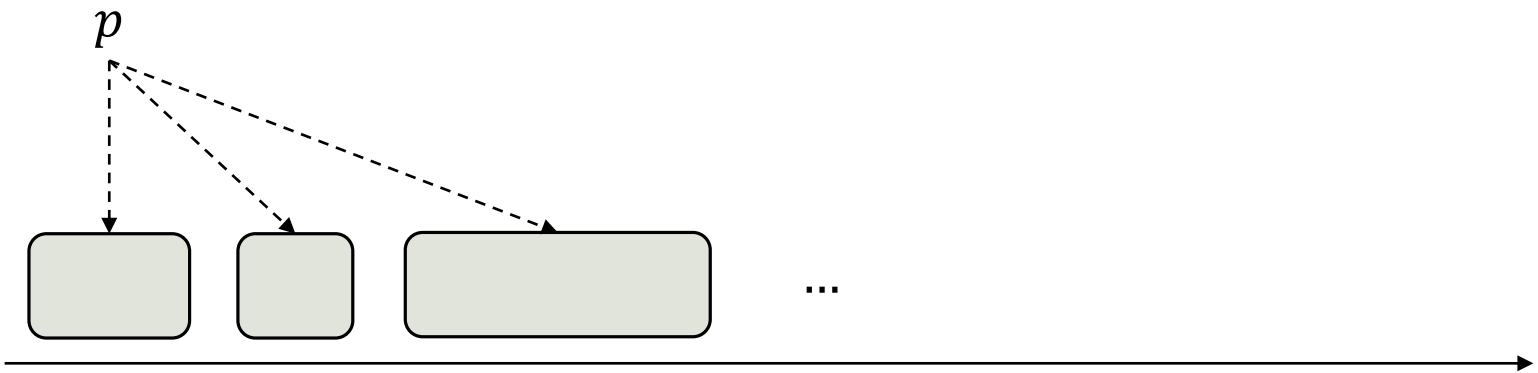

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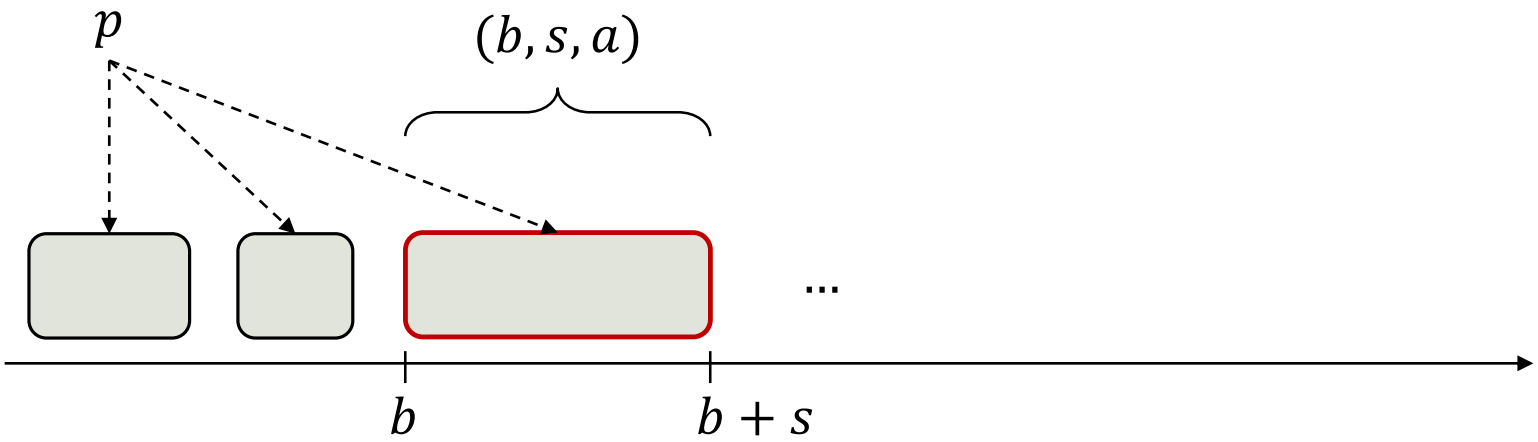

Memory Operations

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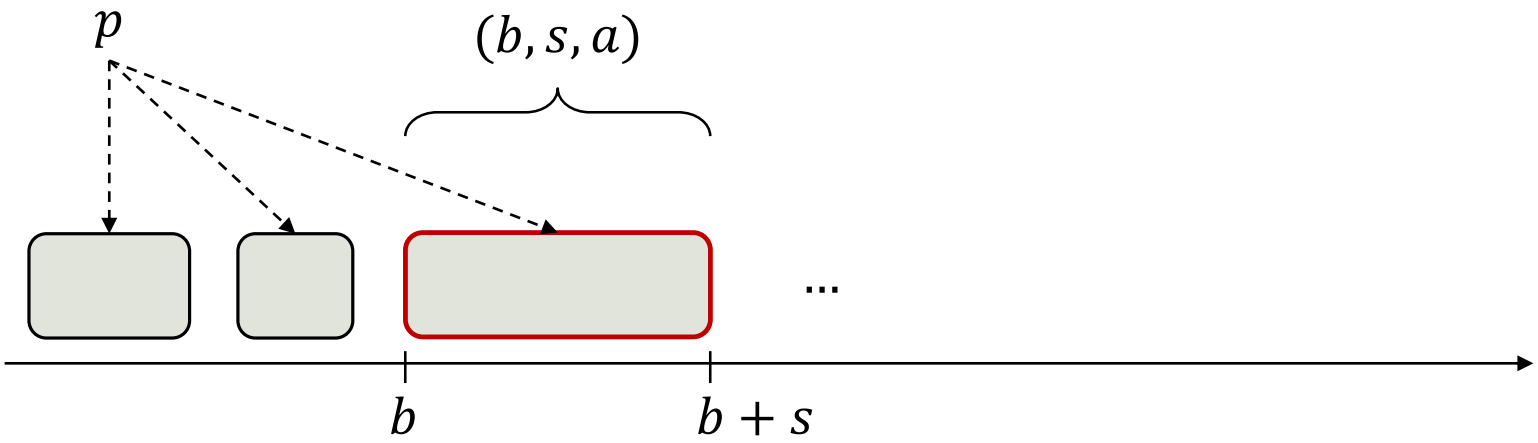
$$p \geq b \ \wedge \ p < b + s$$



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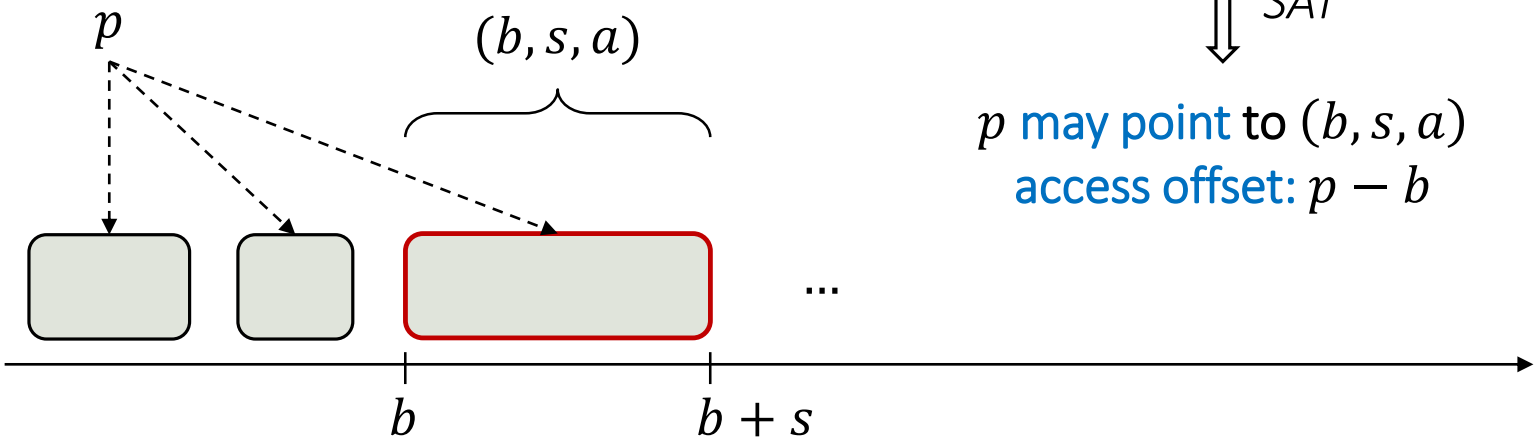


$$p \geq b \ \wedge \ p < b + s$$

$$\Downarrow \ SAT$$

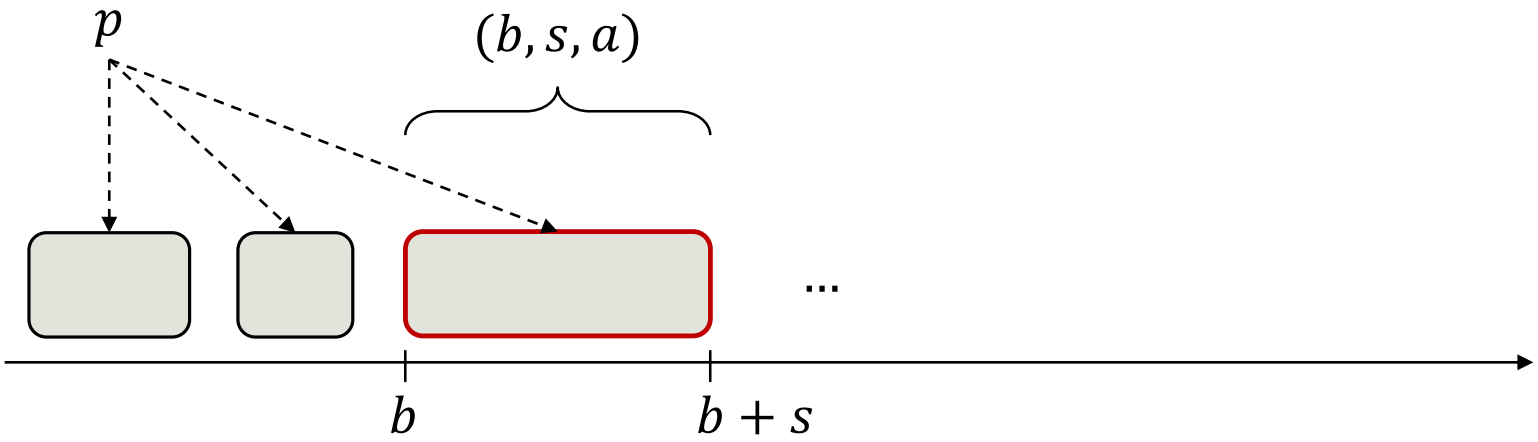
p may point to (b, s, a)

access offset: $p - b$

Memory Operations

Deallocate p

$p = b$



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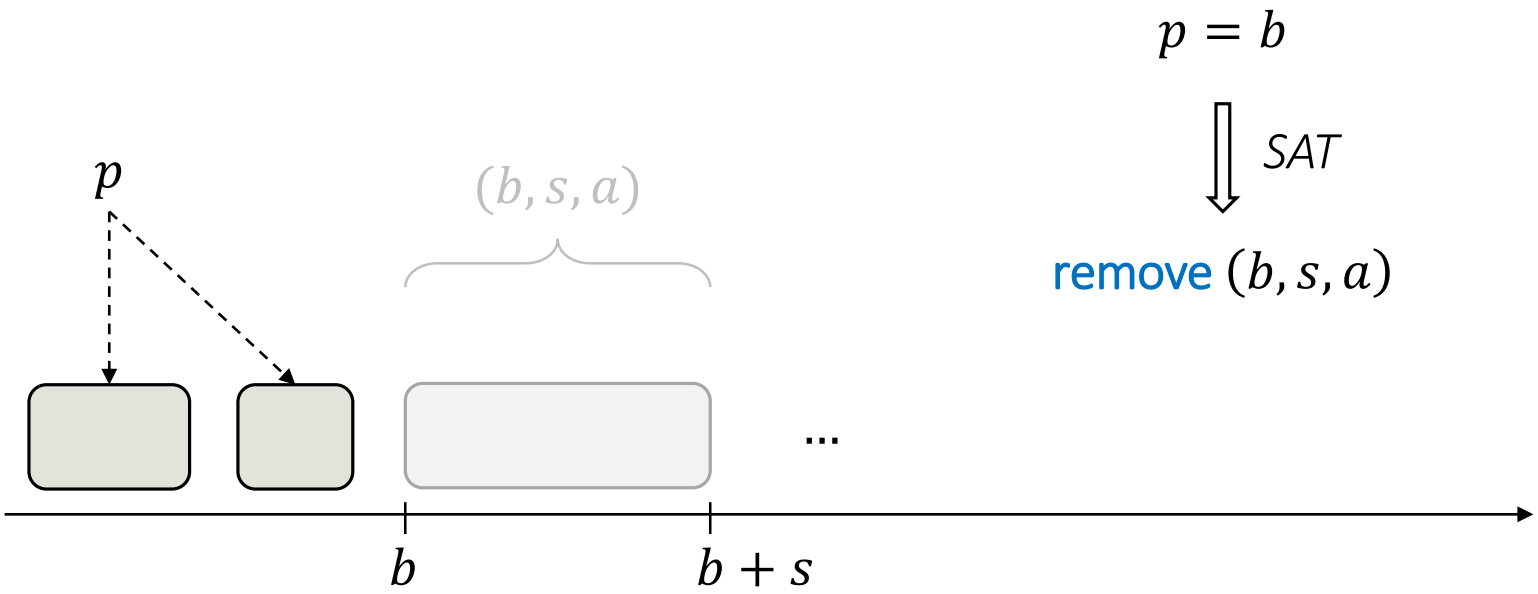

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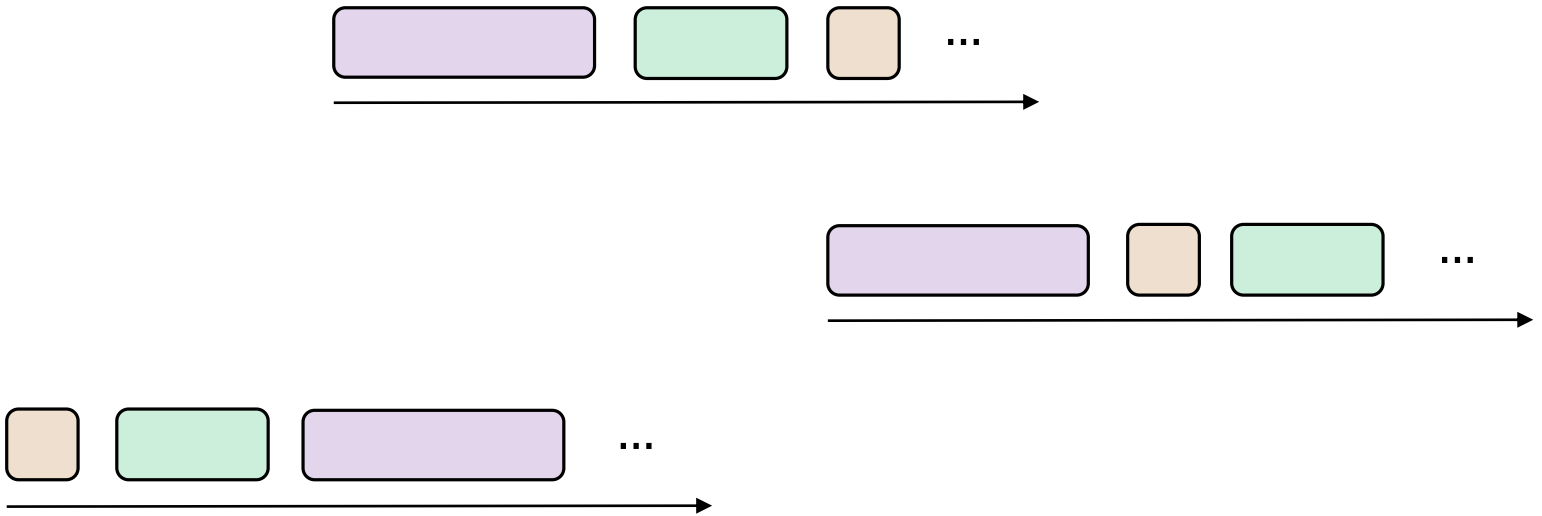
Observation

Specific address values don’t matter



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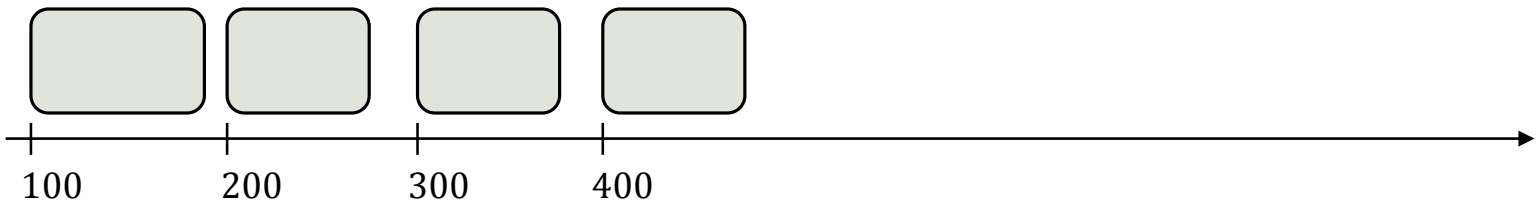

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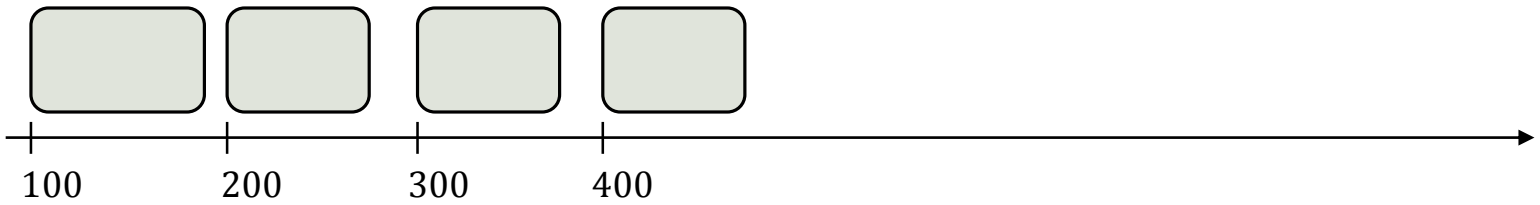

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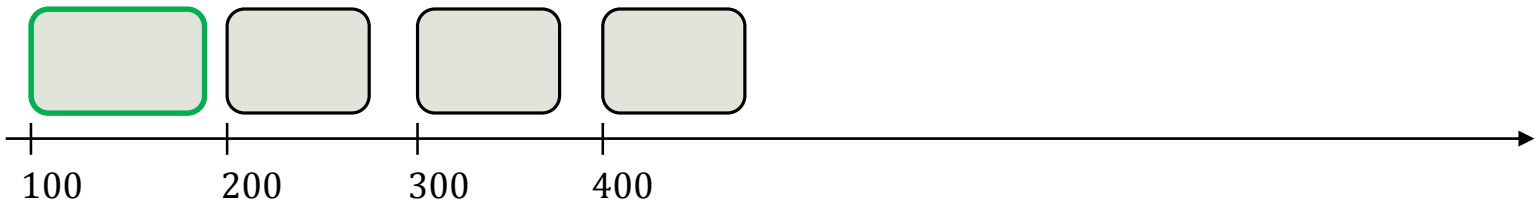
resolution query

$$i < 2 \wedge j < 10 \wedge 100 \leq p < 112$$

SAT



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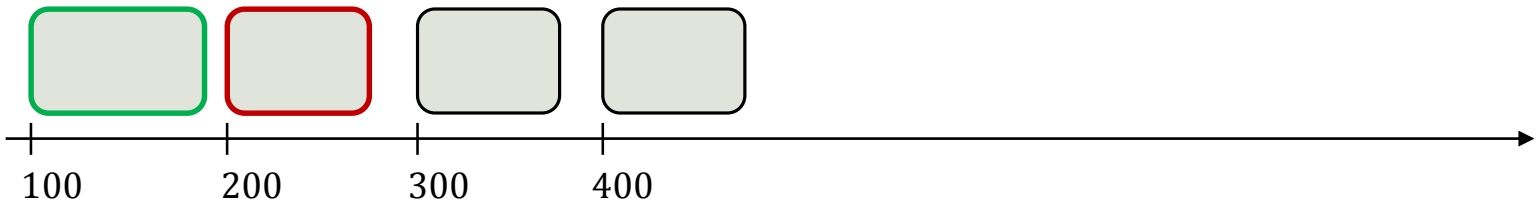
resolution query

$$i < 2 \wedge j < 10 \wedge 200 \leq p < 210$$

UNSAT



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$$i < 2 \wedge j < 10 \wedge 300 \leq p < 310$$

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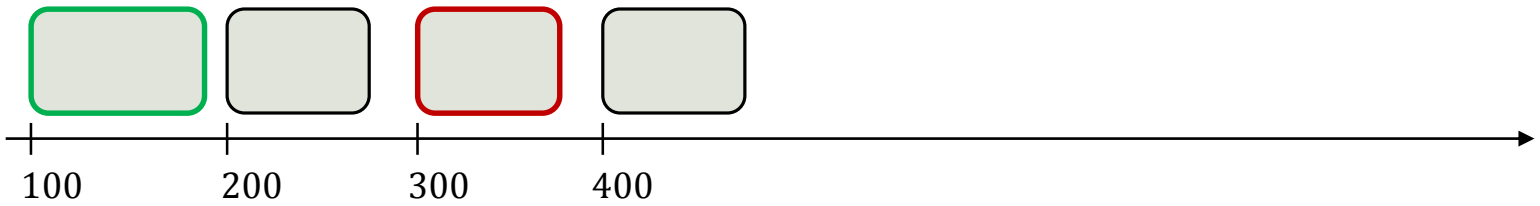

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resolution query

$$i < 2 \wedge j < 10 \wedge 400 \leq p < 410$$

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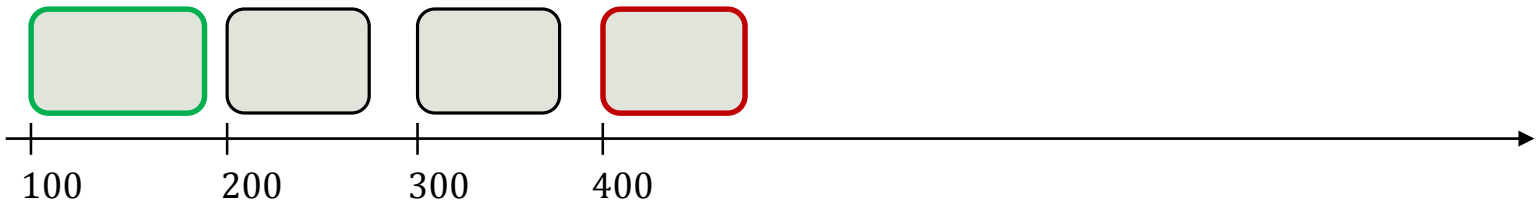

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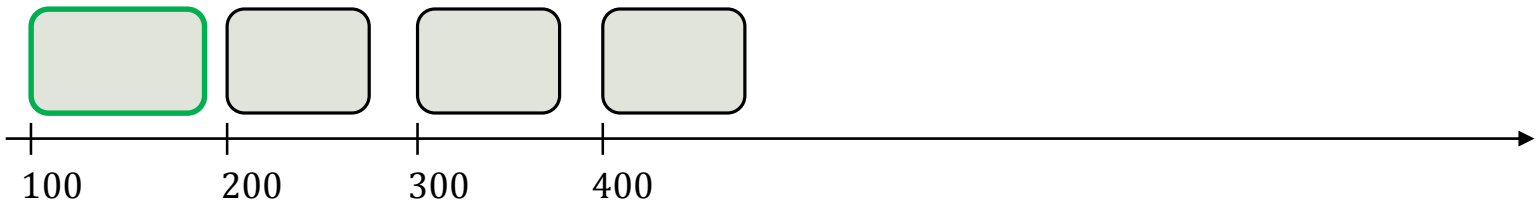

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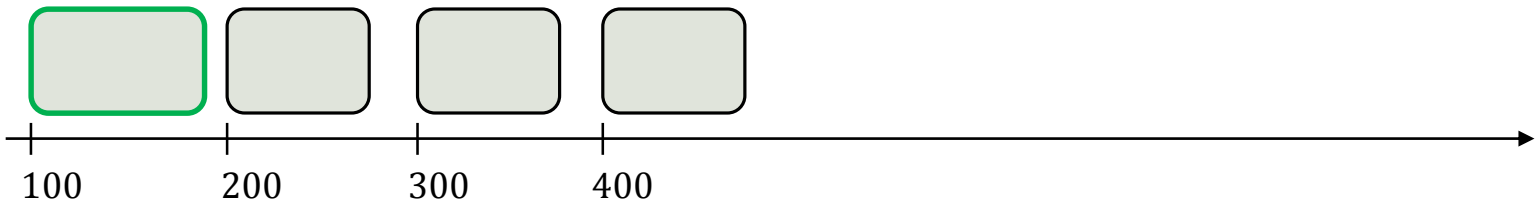
$select(a[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400\,], p - 100)$



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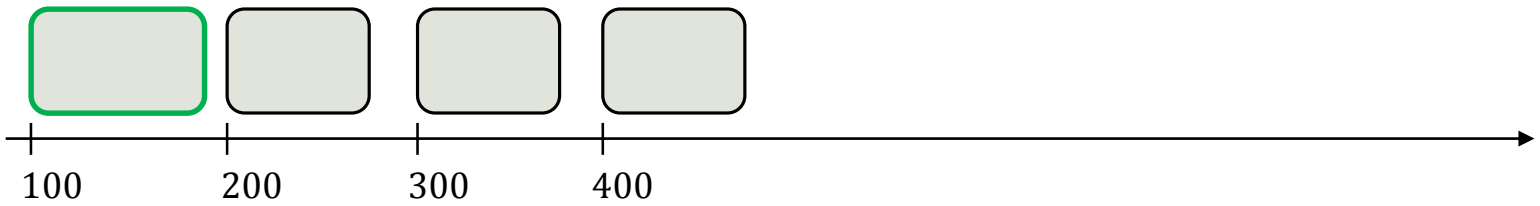

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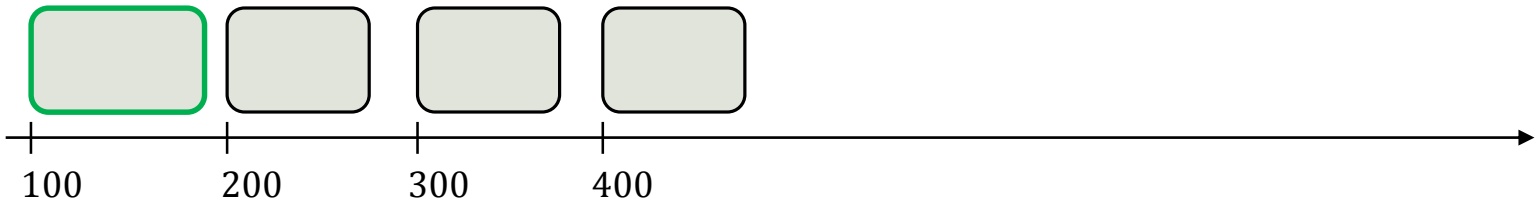
$select(a[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i * 4) + j$



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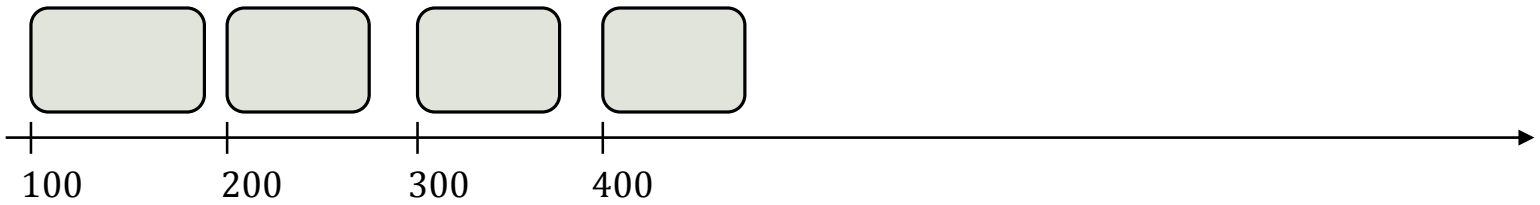

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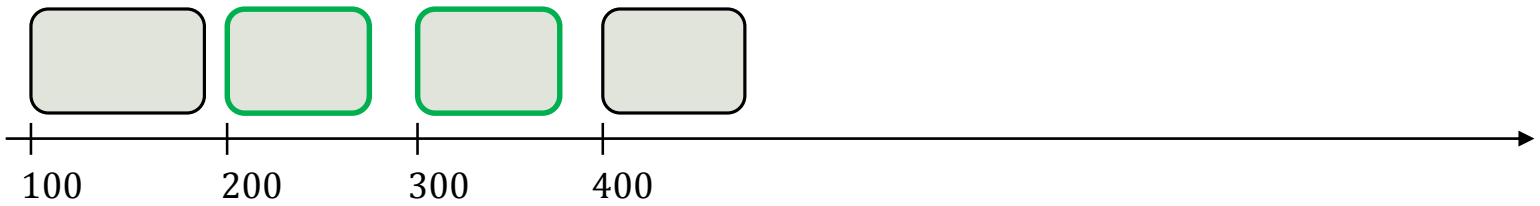
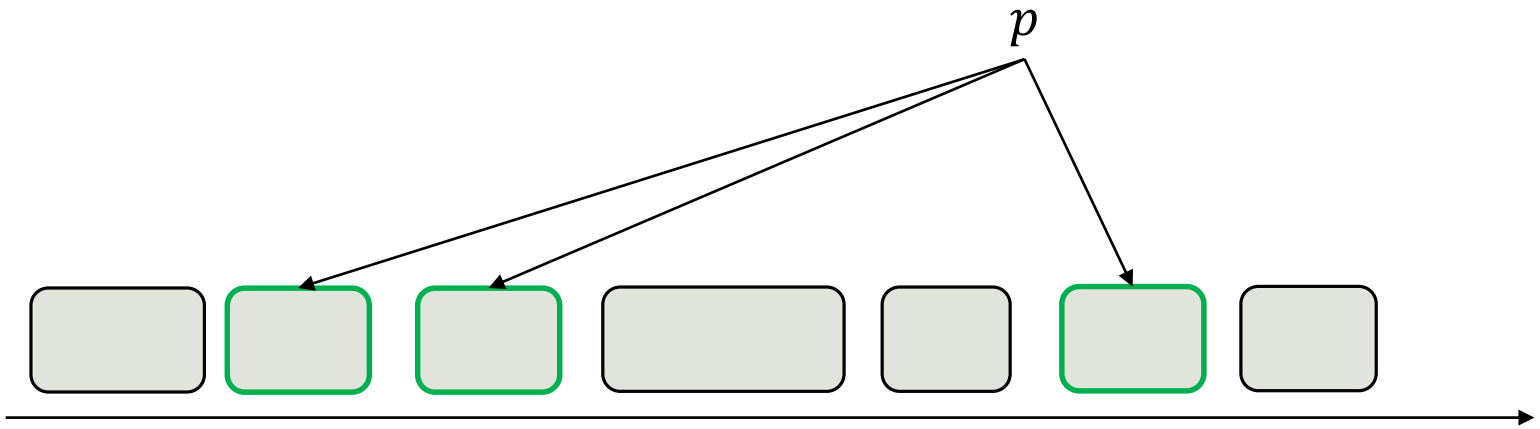


Multiple Resolutions

Approaches:

- Forking
- Merging



Segmented Memory Model

- Introduced by *Kapus et al.* (FSE 2019)
- Partitions the memory into segments using pointer analysis
- Pointer dereference without forking
  - Any pointer is resolved to at most one segment

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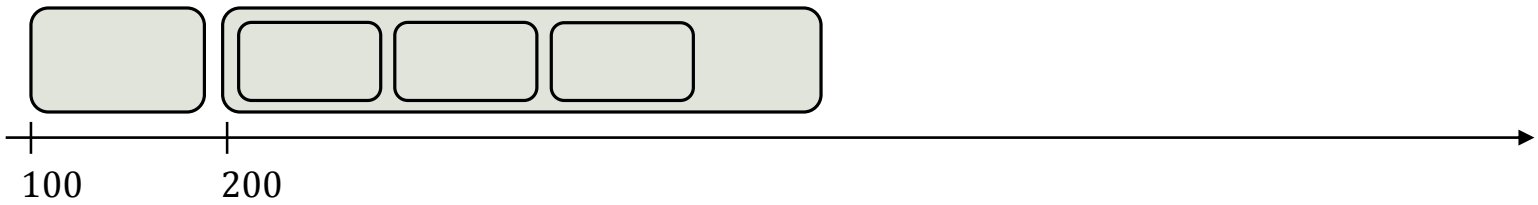

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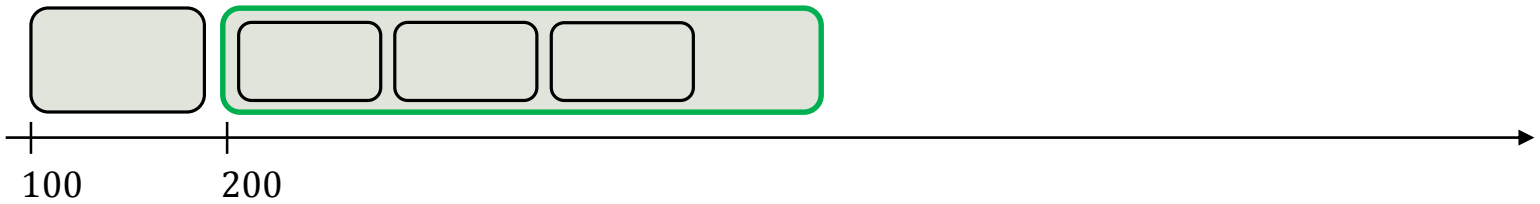

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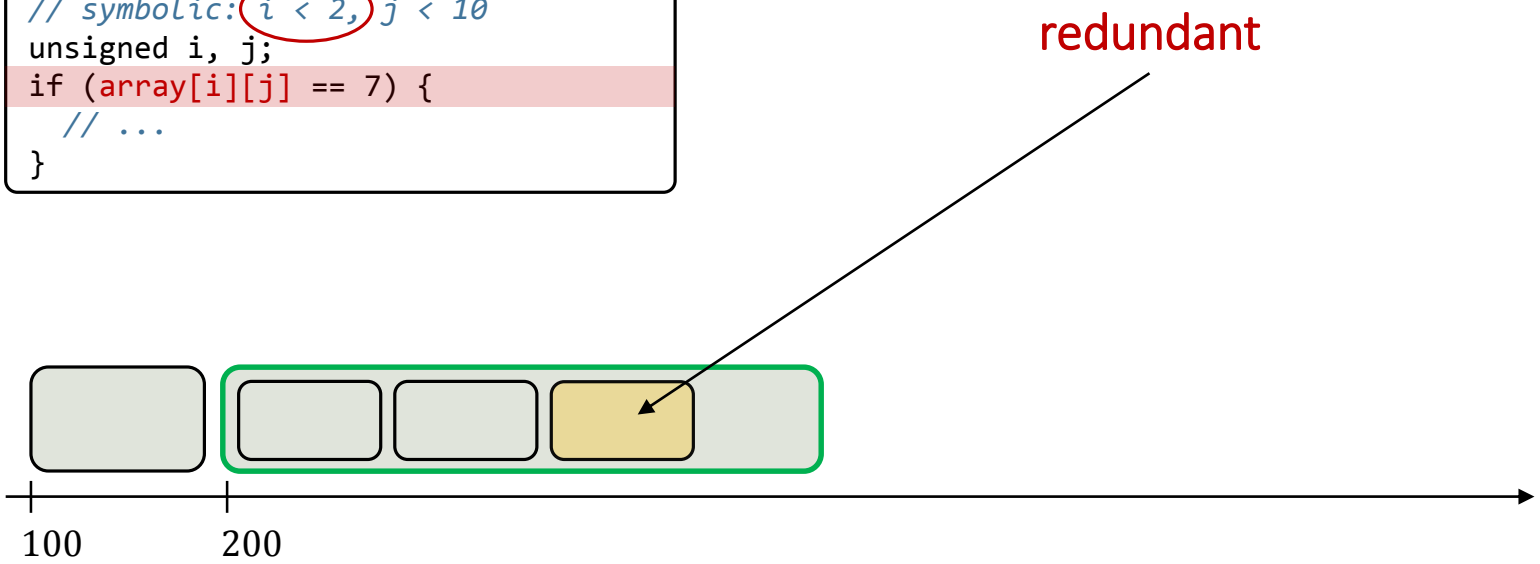
// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

$$p \stackrel{\text{def}}{=} select(a[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i * 4) + j$$

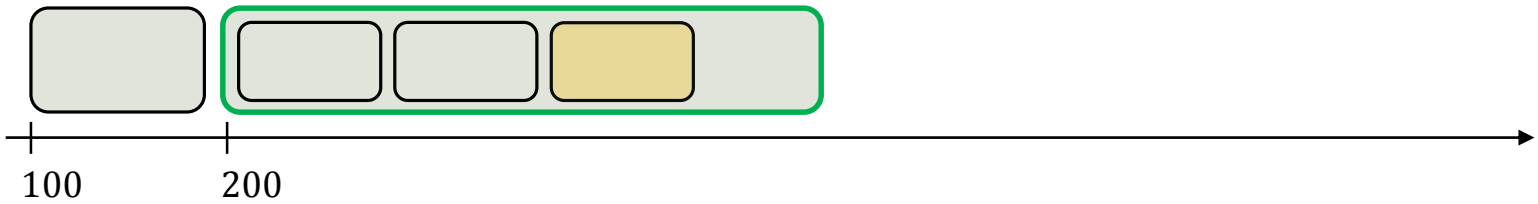


Segmented Memory Model

```
char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

- Avoids forking
- Unnecessarily large segments
- Slower constraint solving



Relocatable Memory Model

Memory objects:

- Defined by a tuple (β, s, a)
- Base addresses are symbolic

Address space:

- Maintain address constraints
- Preserves the *non-overlapping* property

Relocatable Memory Model

```
char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

address constraints:

$\beta_1 = 100$

Relocatable Memory Model

```
char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

address constraints:

$\beta_1 = 100 \wedge \beta_2 = 200$

Relocatable Memory Model

```
char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

address constraints:

$\beta_1 = 100 \wedge \beta_2 = 200 \wedge \beta_3 = 300$

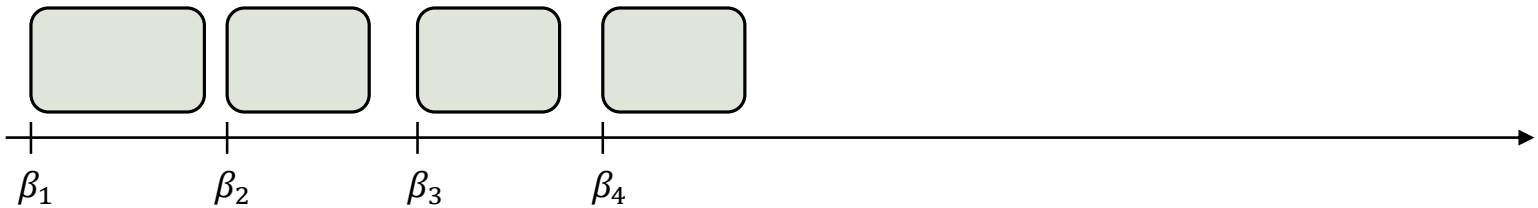
Relocatable Memory Model

```
char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

address constraints:

$\beta_1 = 100 \wedge \beta_2 = 200 \wedge \beta_3 = 300 \wedge \beta_4 = 400$



Relocatable Memory Model

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  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```
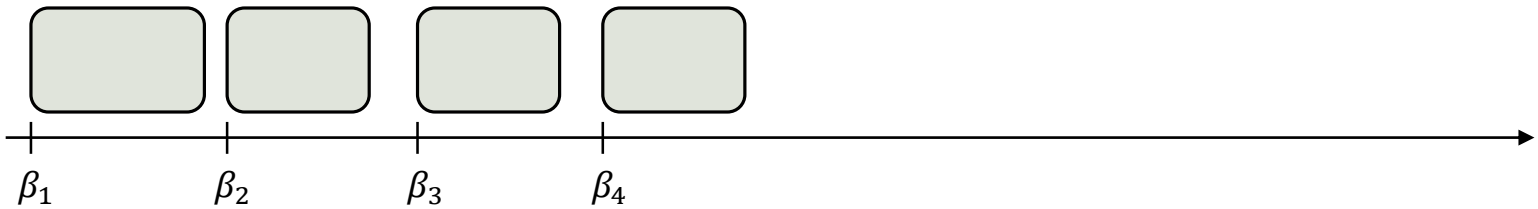

$$p \stackrel{\text{def}}{=} select(a[0 \mapsto \beta_2, 1 \mapsto \beta_3, 2 \mapsto \beta_4], i * 4) + j$$

address constraints:

$$\beta_1 = 100 \wedge \beta_2 = 200 \wedge \beta_3 = 300 \wedge \beta_4 = 400$$



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unsigned i, j;
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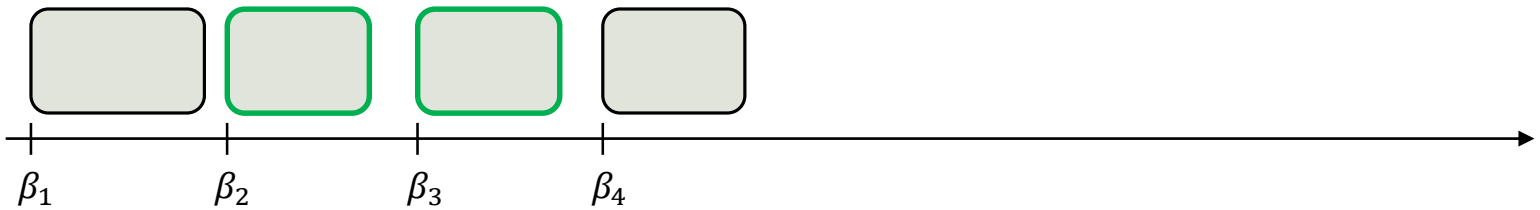

Relocatable Memory Model

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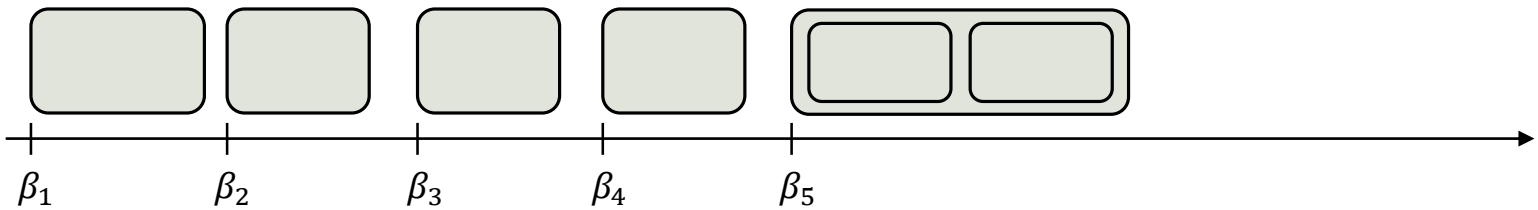
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

$$p \stackrel{\text{def}}{=} select(a[0 \mapsto \beta_2, 1 \mapsto \beta_3, 2 \mapsto \beta_4], i * 4) + j$$

address constraints:

$$\beta_1 = 100 \wedge \beta_2 = 200 \wedge \beta_3 = 300 \wedge \beta_4 = 400 \wedge \beta_5 = 500$$



Relocatable Memory Model

```
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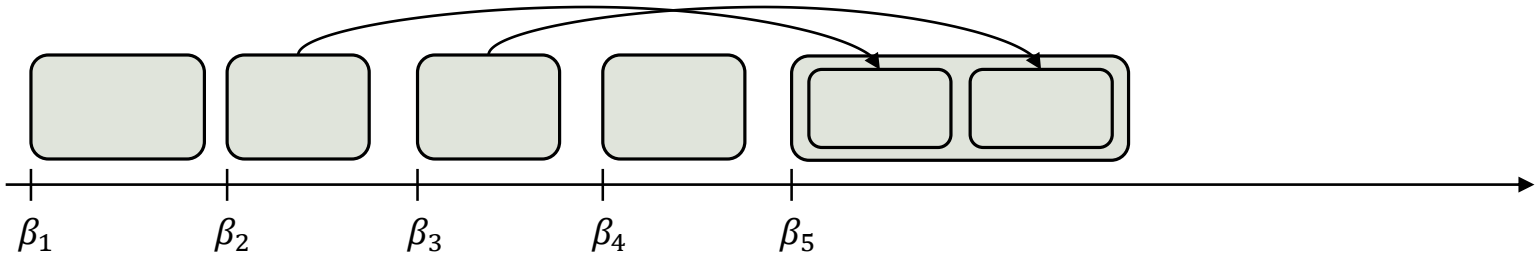
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

$$p \stackrel{\text{def}}{=} select(a[0 \mapsto \beta_2, 1 \mapsto \beta_3, 2 \mapsto \beta_4], i * 4) + j$$

address constraints:

$$\beta_1 = 100 \wedge \beta_2 = 200 \wedge \beta_3 = 300 \wedge \beta_4 = 400 \wedge \beta_5 = 500$$



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}

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unsigned i, j;
if (array[i][j] == 7) {
  // ...
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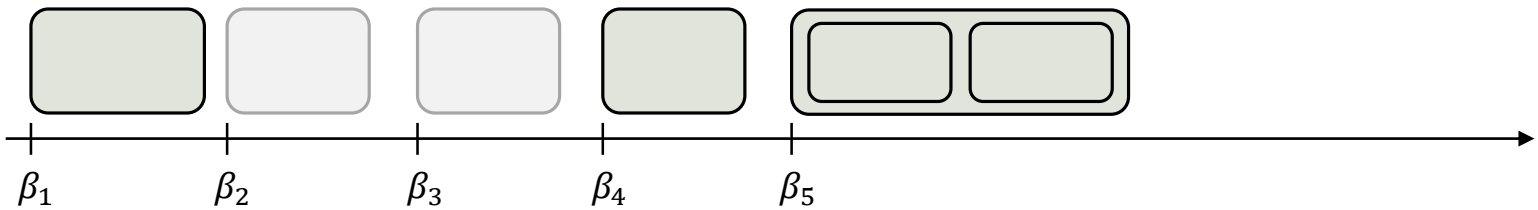
address constraints:

$$\beta_1 = 100 \wedge \beta_2 = 200 \wedge \beta_3 = 300 \wedge \beta_4 = 400 \wedge \beta_5 = 500$$



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  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```
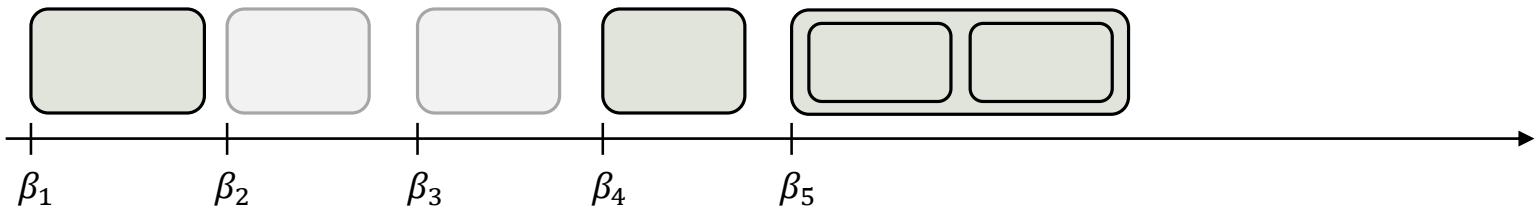

$$p \stackrel{\text{def}}{=} select(a[0 \mapsto \beta_2, 1 \mapsto \beta_3, 2 \mapsto \beta_4], i * 4) + j$$

address constraints:

$$\beta_1 = 100 \wedge \beta_2 = 500 \wedge \beta_3 = 510 \wedge \beta_4 = 400 \wedge \beta_5 = 500$$



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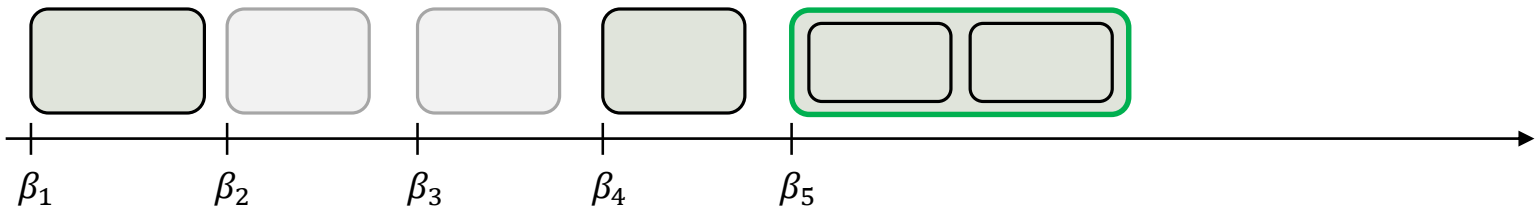
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

$$p \stackrel{\text{def}}{=} select(a[0 \mapsto \beta_2, 1 \mapsto \beta_3, 2 \mapsto \beta_4], i * 4) + j$$

address constraints:

$$\beta_1 = 100 \wedge \beta_2 = 500 \wedge \beta_3 = 510 \wedge \beta_4 = 400 \wedge \beta_5 = 500$$



Relocatable Memory Model

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for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```
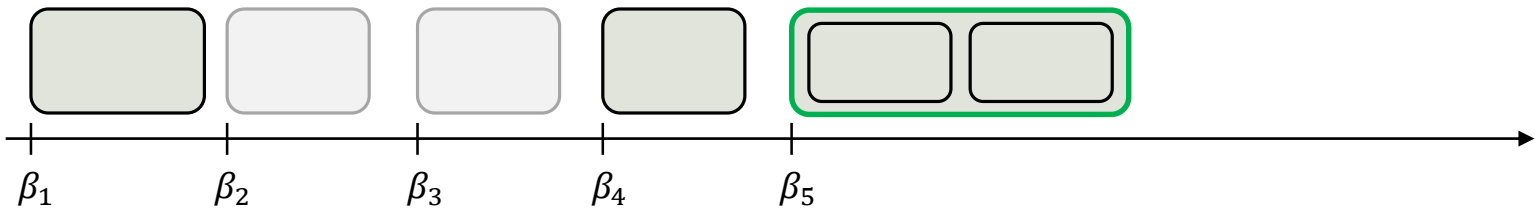

- Avoids forking
- Smaller segments
- Faster constraint solving



Evaluation

Implemented on top of *KLEE*

Benchmarks:

- m4, make, sqlite, apr

Segment size:

- Average reduction: 83%

Average speedup in analysis time:

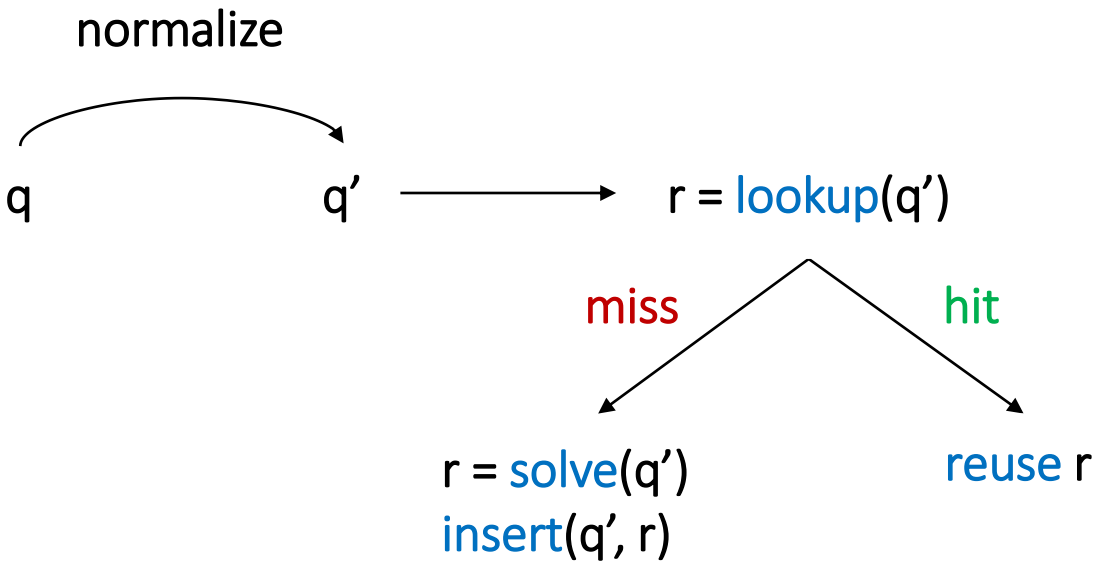
- Relocatable vs. Forking: 2.7X
- Relocatable vs. Segmented: 3.0X

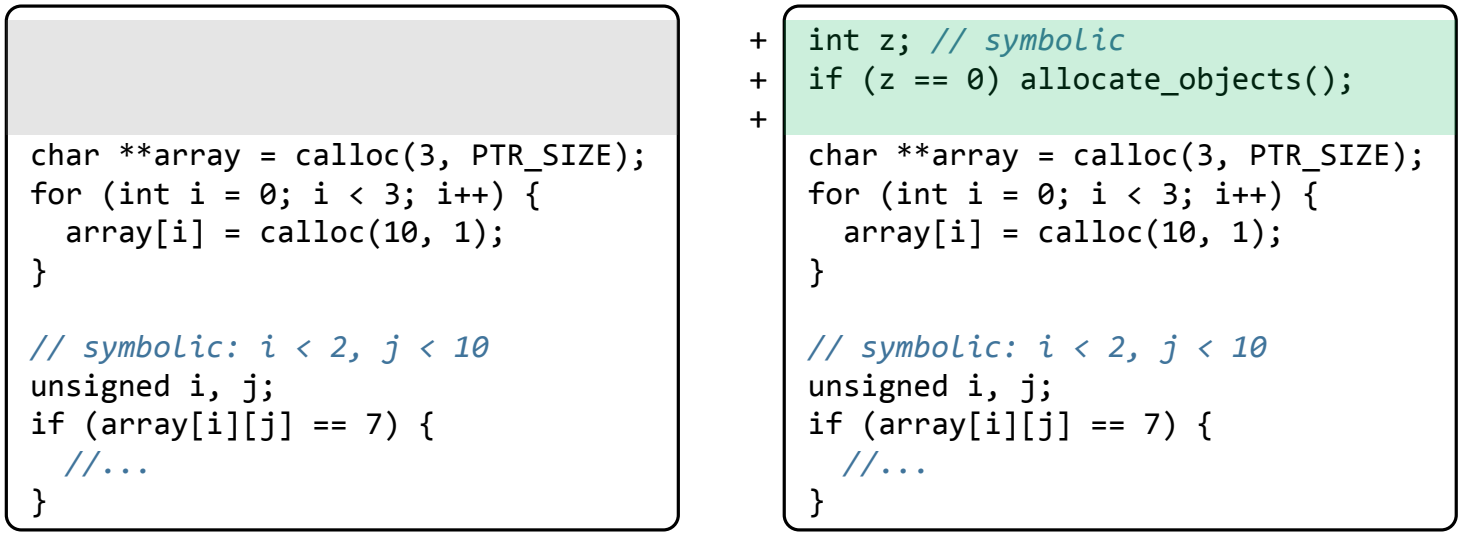
Outline

- Background
  - Symbolic execution
  - Memory model
- Symbolic base addresses
  - Relocatable memory model
  - Address-aware query caching
- Symbolic-size allocations
  - Bounded symbolic-size model
  - State merging with quantifiers
- Conclusions and future work

Query Caching

A common technique for accelerating constraint solving




char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
array[i] = calloc(10, 1);
}
// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
//...
}
+ int z; // symbolic
+ if (z == 0) allocate_objects();
+
char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
array[i] = calloc(10, 1);
}
// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
//...
}

Address-Dependent Queries

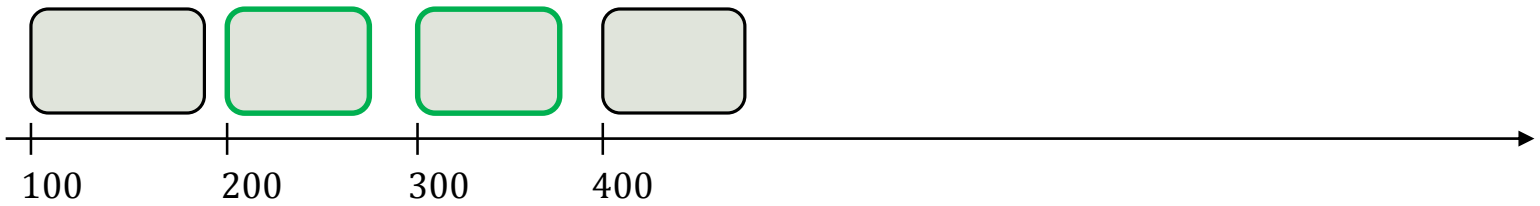
```
int z; // symbolic
if (z == 0) allocate_objects();

char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  //...
}
```

What happens when $z \neq 0$?

Address-Dependent Queries

```
int z; // symbolic
if (z == 0) allocate_objects();

char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  //...
}
```

$$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i * 4) + j$$



Address-Dependent Queries

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int z; // symbolic
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// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
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}
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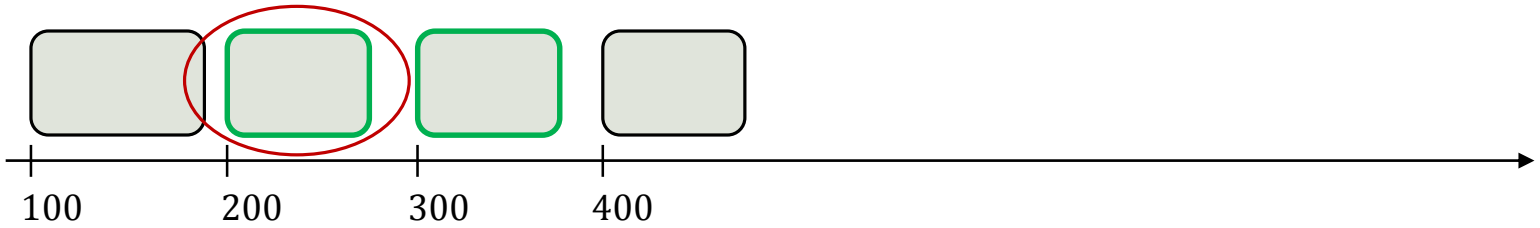

Address-Dependent Queries

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char **array = calloc(3, PTR_SIZE);
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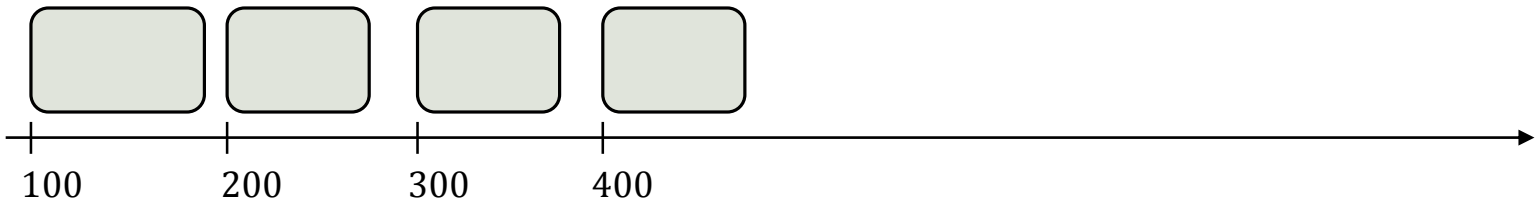
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  //...
}
```

$$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i * 4) + j$$

path constraints:

$$pc \stackrel{\text{def}}{=} z \neq 0 \ \wedge \ i < 2 \ \wedge \ j < 10 \ \wedge \ 200 \leq p < 210$$



Address-Dependent Queries

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int z; // symbolic
if (z == 0) allocate_objects();

char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
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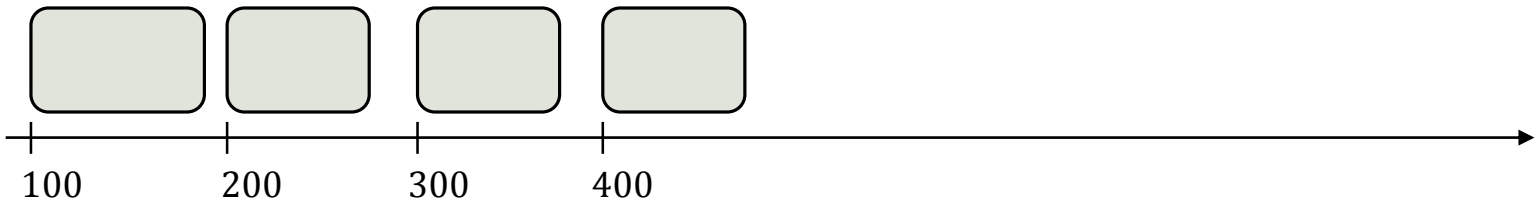
path constraints:

$$pc \stackrel{\text{def}}{=} z \neq 0 \ \wedge \ i < 2 \ \wedge \ j < 10 \ \wedge \ 200 \leq p < 210$$

query:

$$pc \ \wedge \ select(a_2, p - 200) = 7$$



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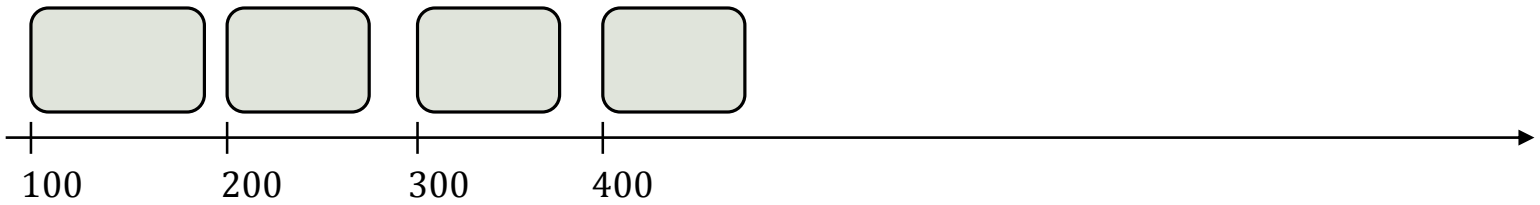

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// symbolic: i < 2, j < 10
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if (array[i][j] == 7) {
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}
```

What happens when $z = 0$?

Address-Dependent Queries

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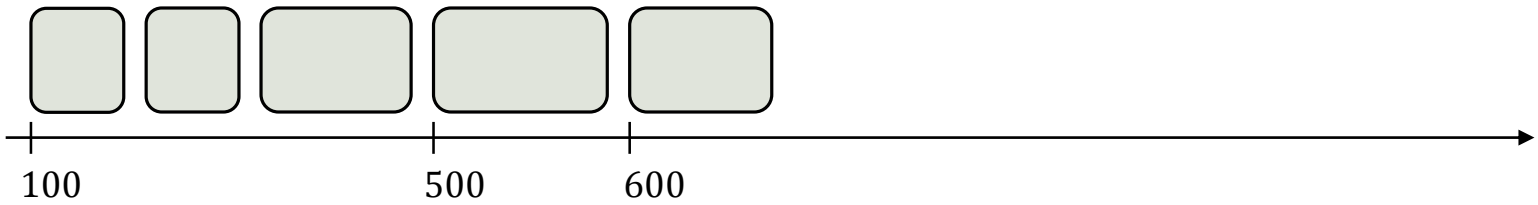

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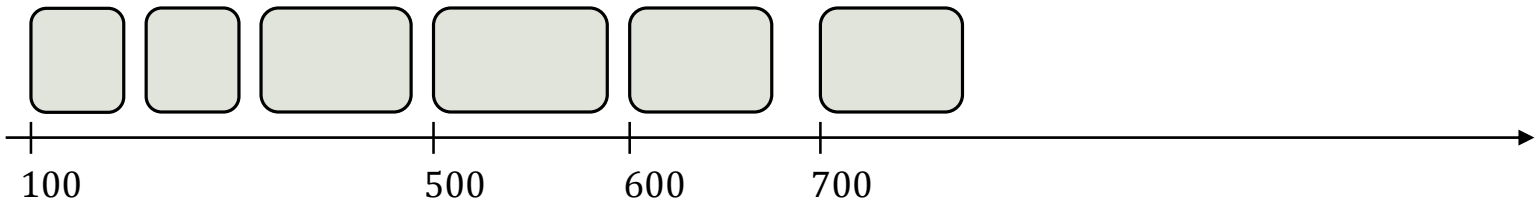

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if (array[i][j] == 7) {
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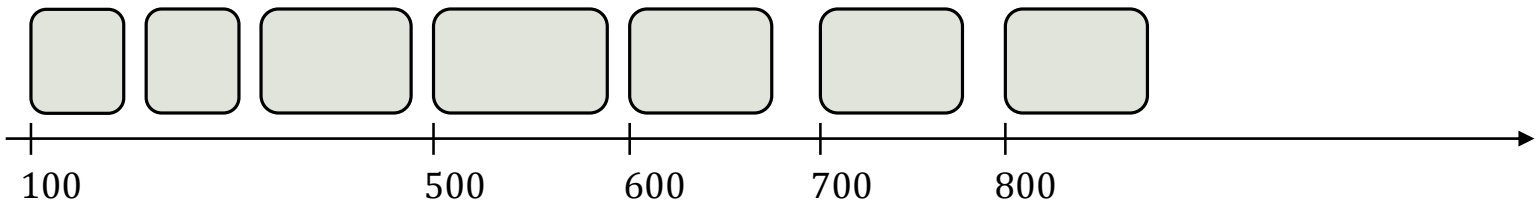

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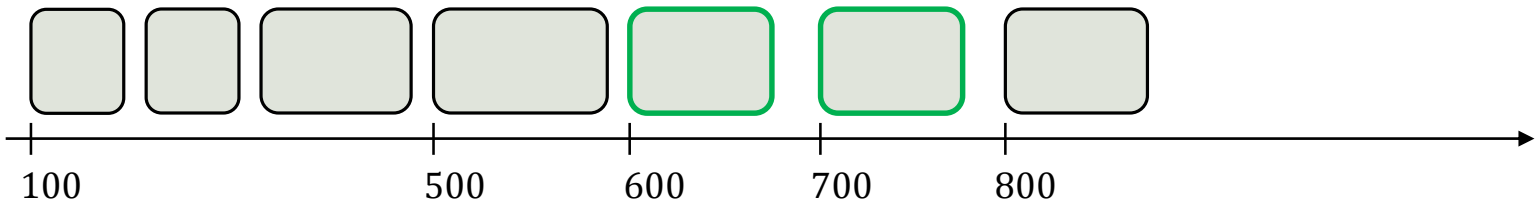
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  //...
}
```

$$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 600, 1 \mapsto 700, 2 \mapsto 800], i * 4) + j$$



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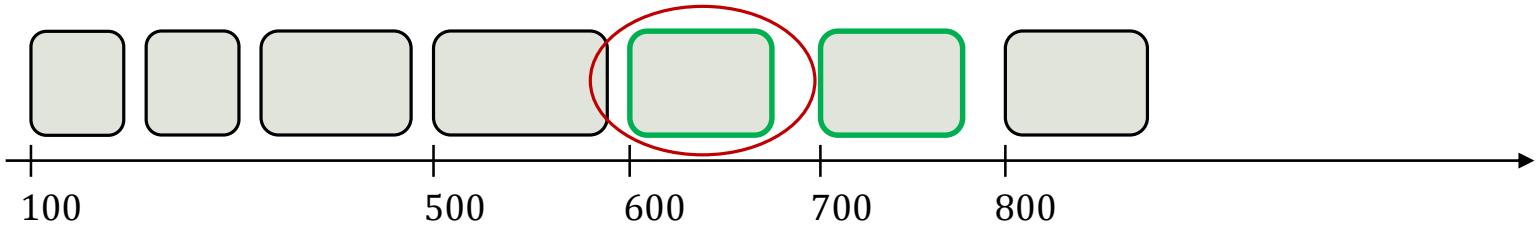

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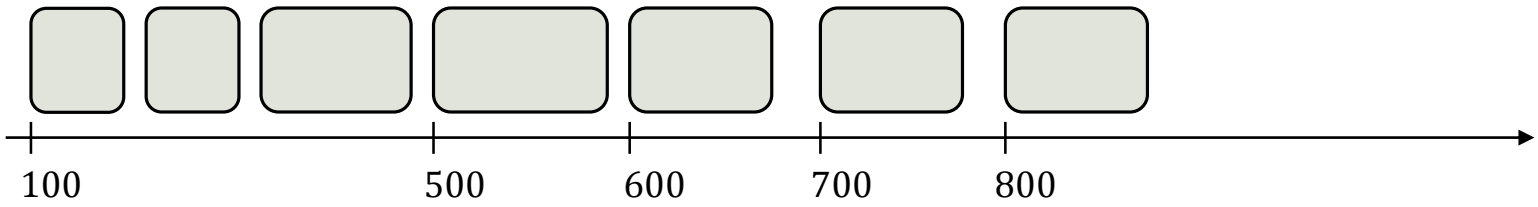
path constraints:

$$pc \stackrel{\text{def}}{=} z = 0 \wedge i < 2 \wedge j < 10 \wedge 600 \leq p < 610$$



Address-Dependent Queries

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int z; // symbolic
if (z == 0) allocate_objects();

char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  //...
}
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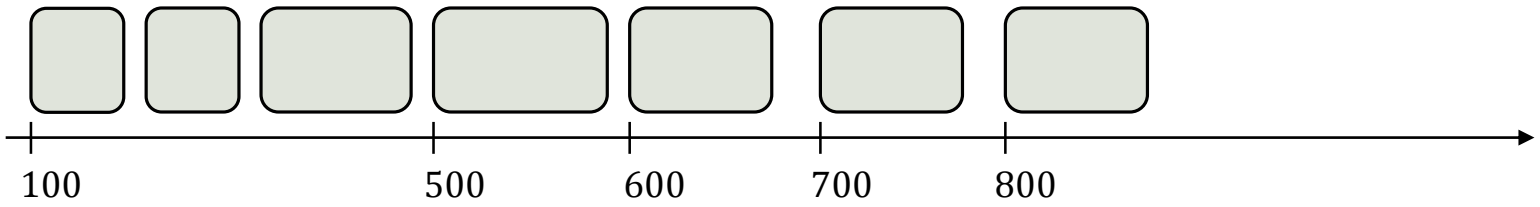
query:

$$pc \ \wedge \ select(a_2, p - 600) = 7$$



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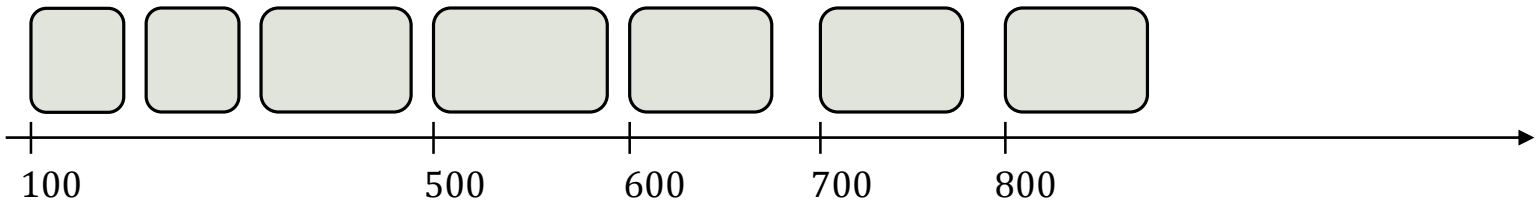

$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i * 4) + j$

$pc \stackrel{\text{def}}{=} z \neq 0 \ \wedge \ i < 2 \ \wedge \ j < 10 \ \wedge \ 200 \leq p < 210$

query:

$pc \ \wedge \ select(a_2, p - 200) = 7$

$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 600, 1 \mapsto 700, 2 \mapsto 800], i * 4) + j$

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query:

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- Equisatisfiable
- Query caching **fails** (No common normal form)

Solution: Relocatable Memory Model

- Base addresses are **symbolic**
  - Distinguish between **integer** and **address** values
- Determine **equisatisfiability** by checking:
  - Expression isomorphism (equality up to renaming)
  - Address space isomorphism

Assuming *that the analyzed program has* ***no undefined behavior****.*

Solution: Relocatable Memory Model

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path constraints:

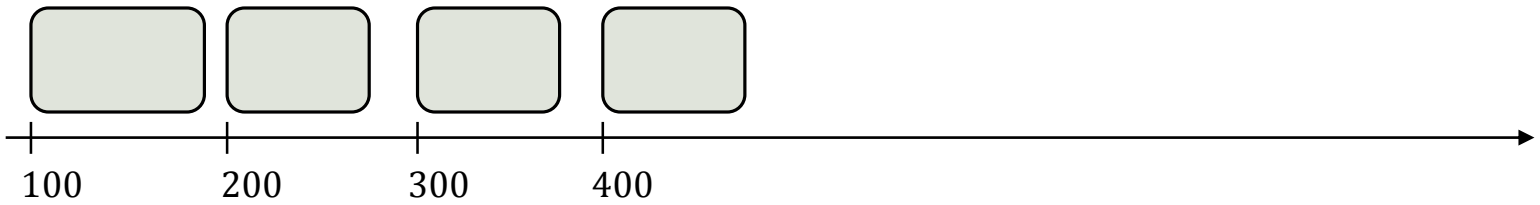
$pc \stackrel{\text{def}}{=} z \neq 0 \ \wedge \ i < 2 \ \wedge \ j < 10 \ \wedge \beta_2 \leq p < \beta_2 + 10$

query:

$pc \ \wedge \ select(a_2, p - \beta_2) = 7$

address constraints:

$\beta_1 = 100 \ \wedge \beta_2 = 200 \wedge \beta_3 = 300 \wedge \beta_4 = 400$



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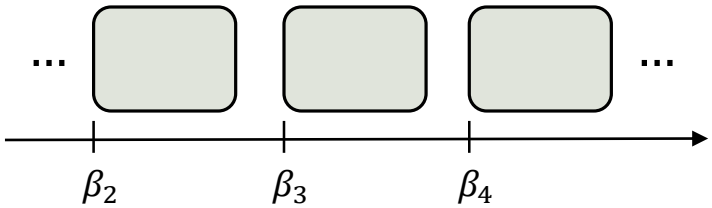

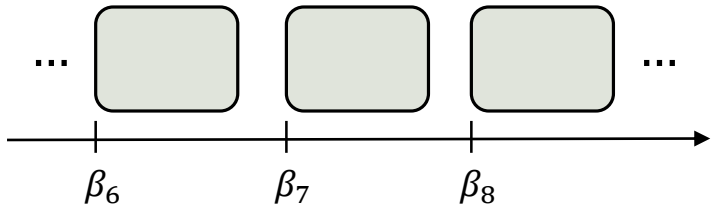

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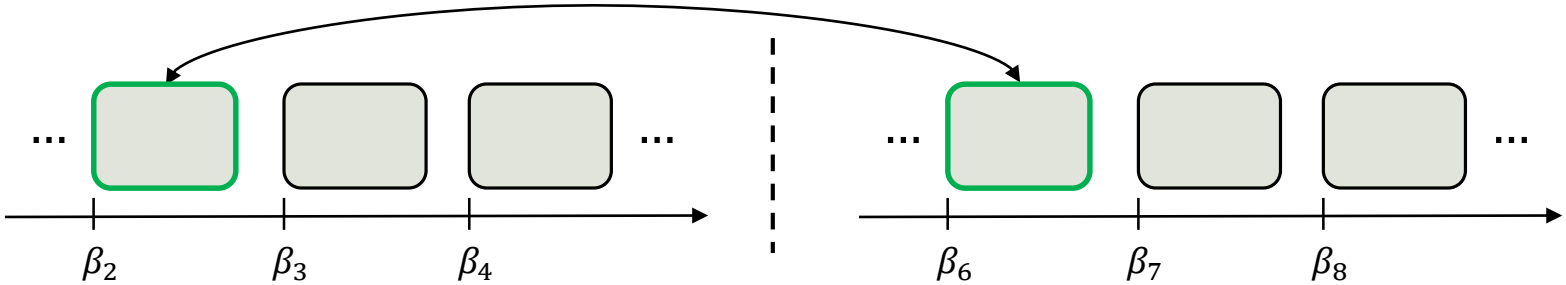

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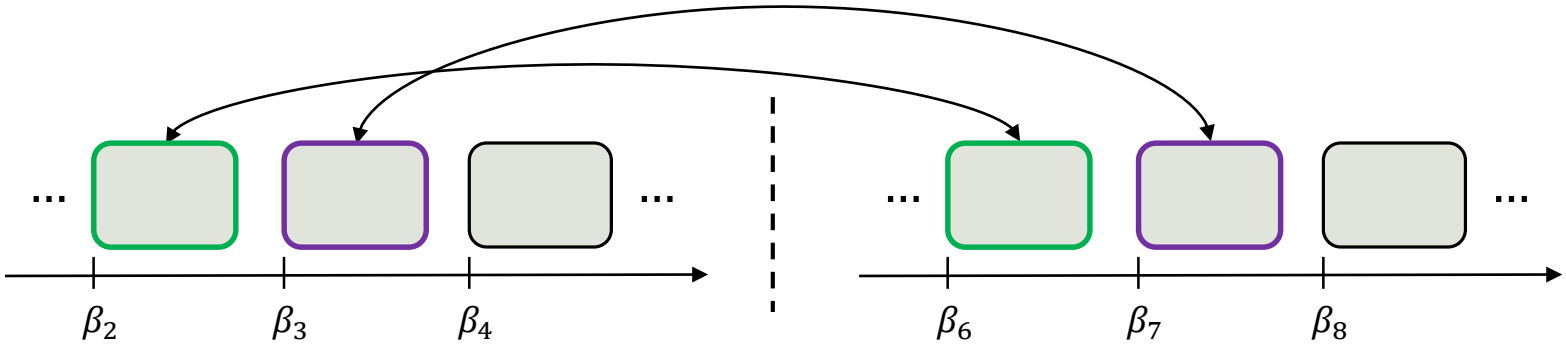

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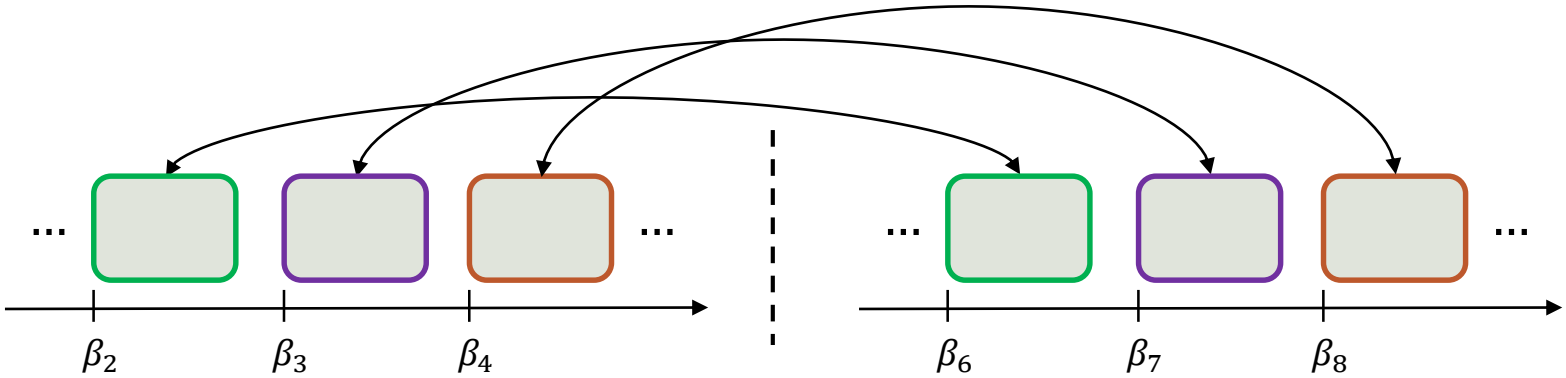


Evaluation

Implemented on top of *KLEE*

Benchmarks:

- m4, make, sqlite, apr, libxml2, expat, bash, json-c

Cache misses (number of queries passed to SMT solver):

- Average reduction: 58%

Analysis speedup in analysis time: 2.2X

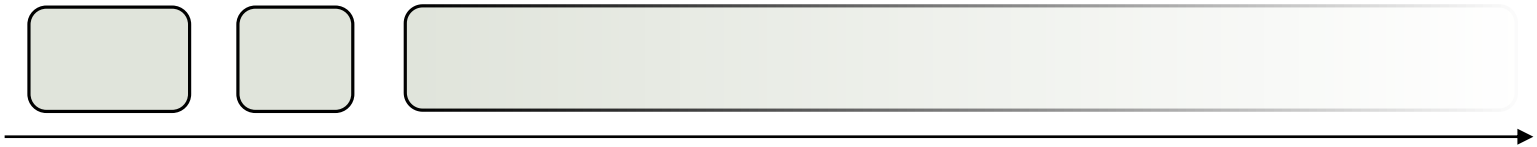
Outline

- Background
  - Symbolic execution
  - Memory model
- Symbolic base addresses
  - Relocatable memory model
  - Address-aware query caching
- Symbolic-size allocations
  - Bounded symbolic-size model
  - State merging with quantifiers
- Conclusions and future work

Observation

Modeling symbolic-size objects is hard:

- Fixed
  - Limited exploration



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Modeling symbolic-size objects is hard:

- Fixed
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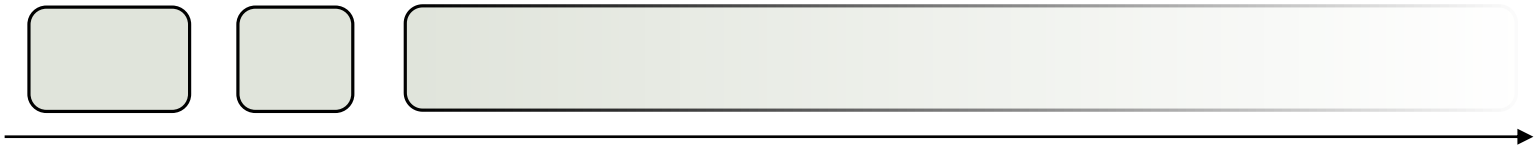
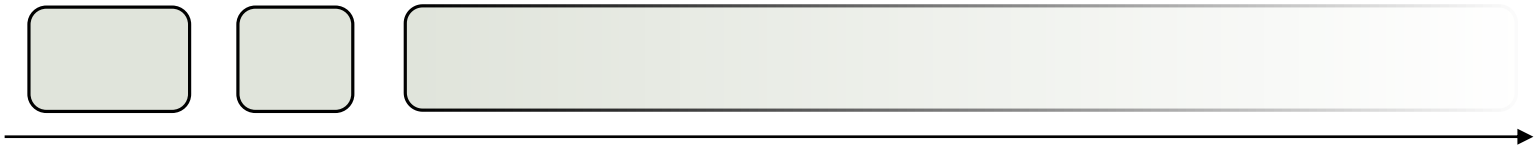
  - Overlapping in a linear address space
  - High memory consumption



Observation

Modeling symbolic-size objects is hard:

- Fixed
  - Limited exploration
- Unbounded
  - Overlapping in a linear address space
  - High memory consumption
- Bounded
  - Integrates with a linear address space
  - Controllable memory consumption



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concretize $k + 1$ to 3

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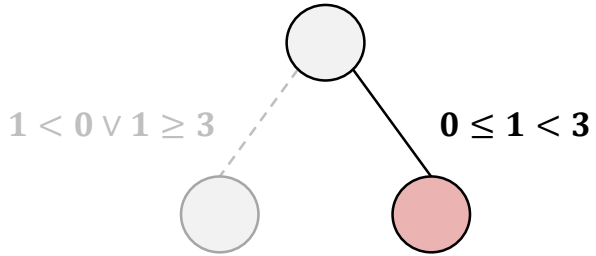

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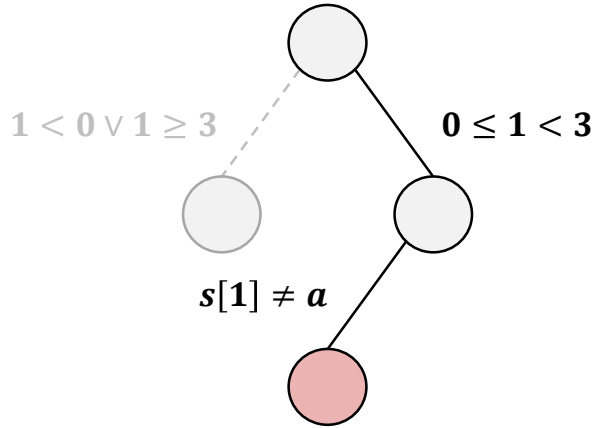

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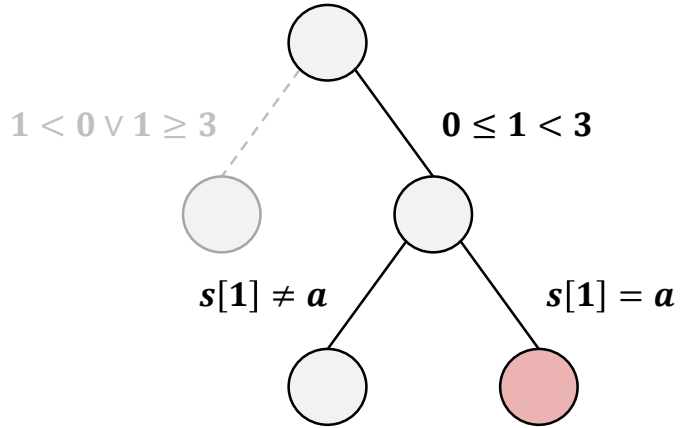

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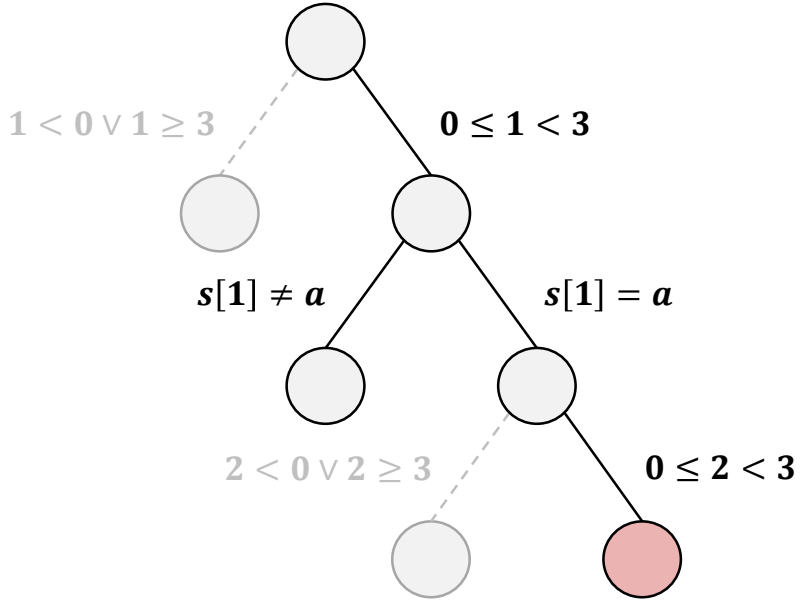

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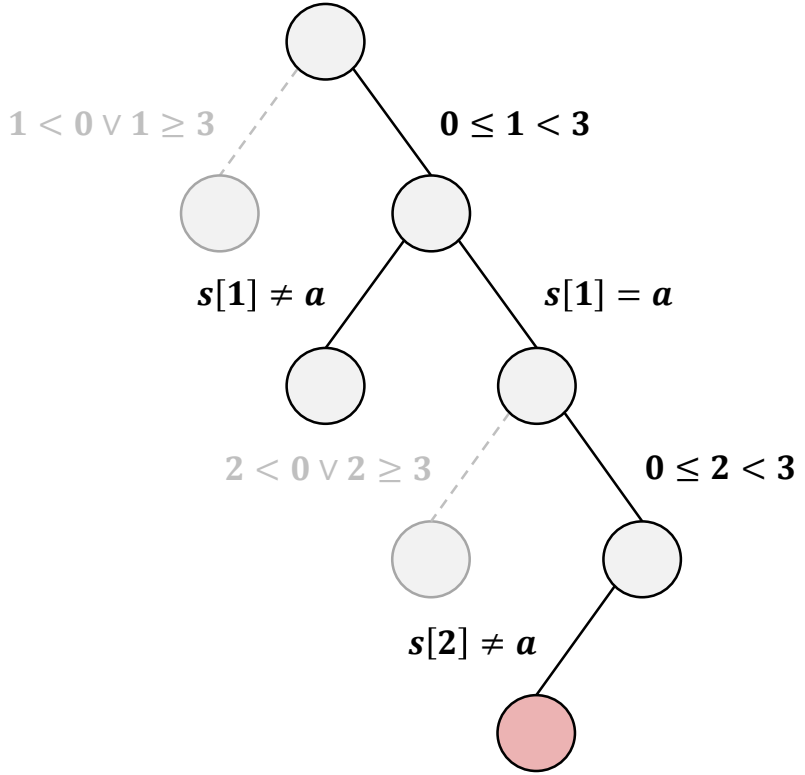

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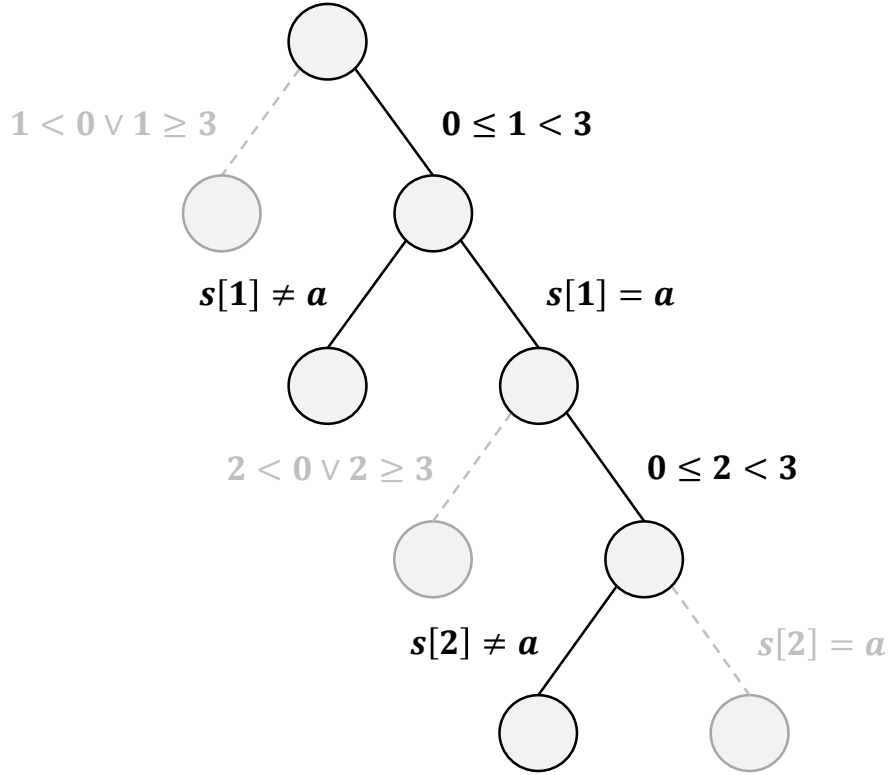

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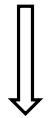

missed bug!

out-of-bounds access if $k = 0$

Bounded Symbolic-Size Model

Defined by a tuple (b, σ, c, a):

- Concrete base address
- Symbolic size
- Concrete capacity: $0 < \sigma \leq c$
- SMT array

Easily integrated with a linear address space

Controllable memory consumption

Example

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char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, 'a');
```

capacity constraint: $0 < k + 1 \leq 3$

Example

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  while (s[count] == c) {
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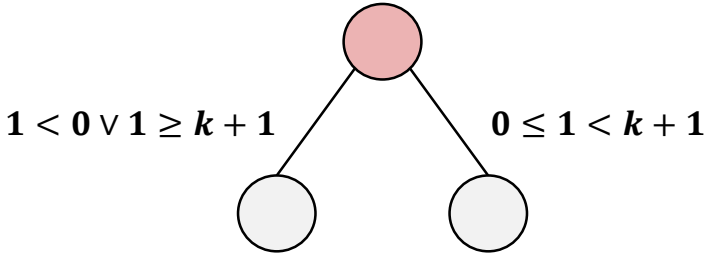
  }
  return count;
}

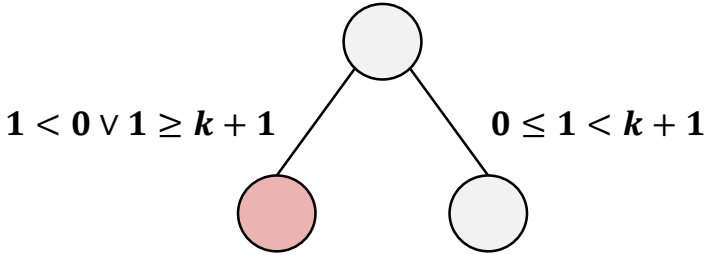
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, ‘a’);
```



Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, ‘a’);
```

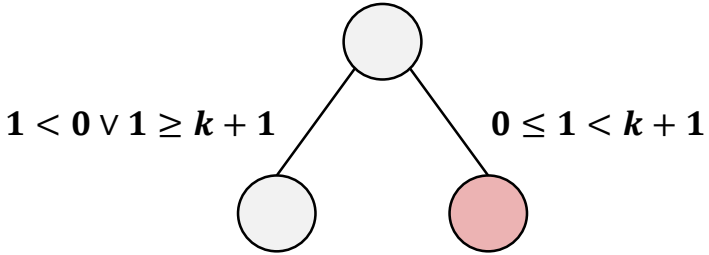


memory error

Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

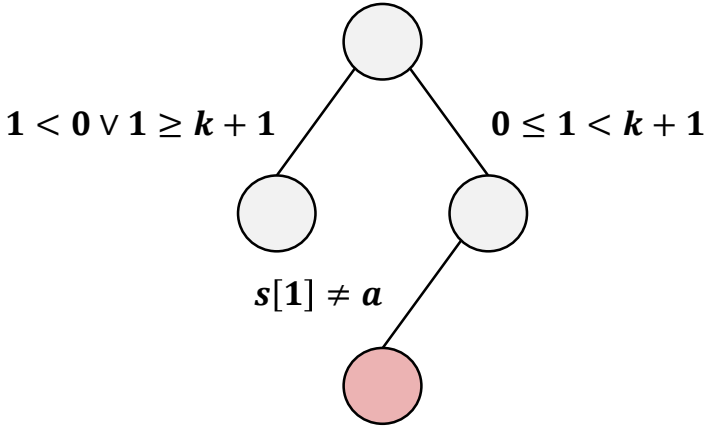
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, ‘a’);
```



Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

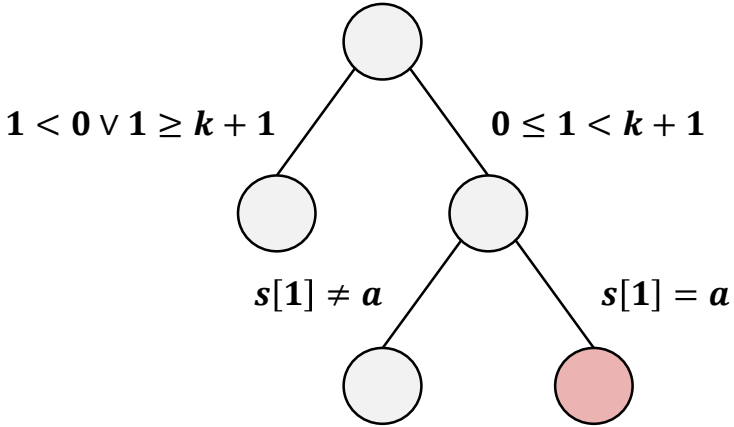
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, ‘a’);
```



Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

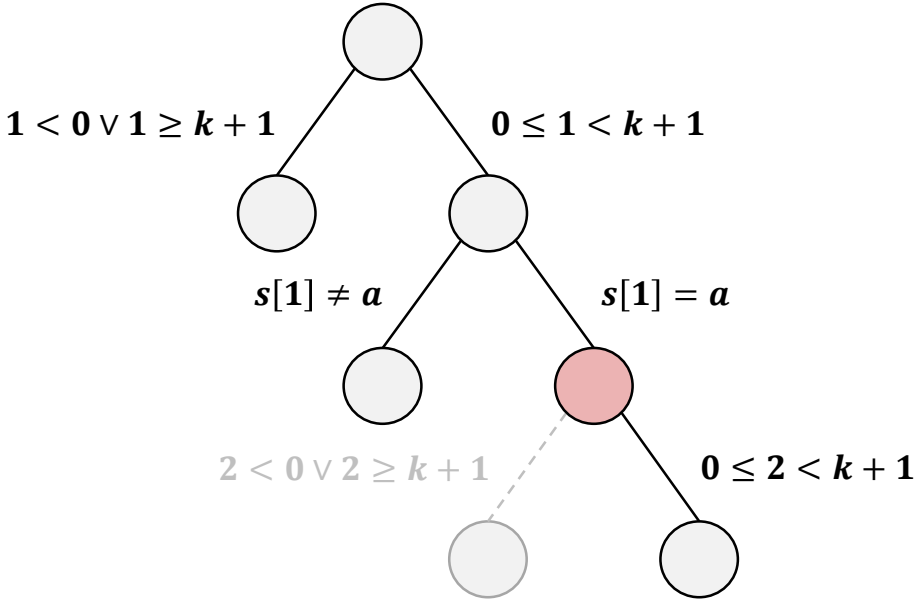
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, ‘a’);
```



Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

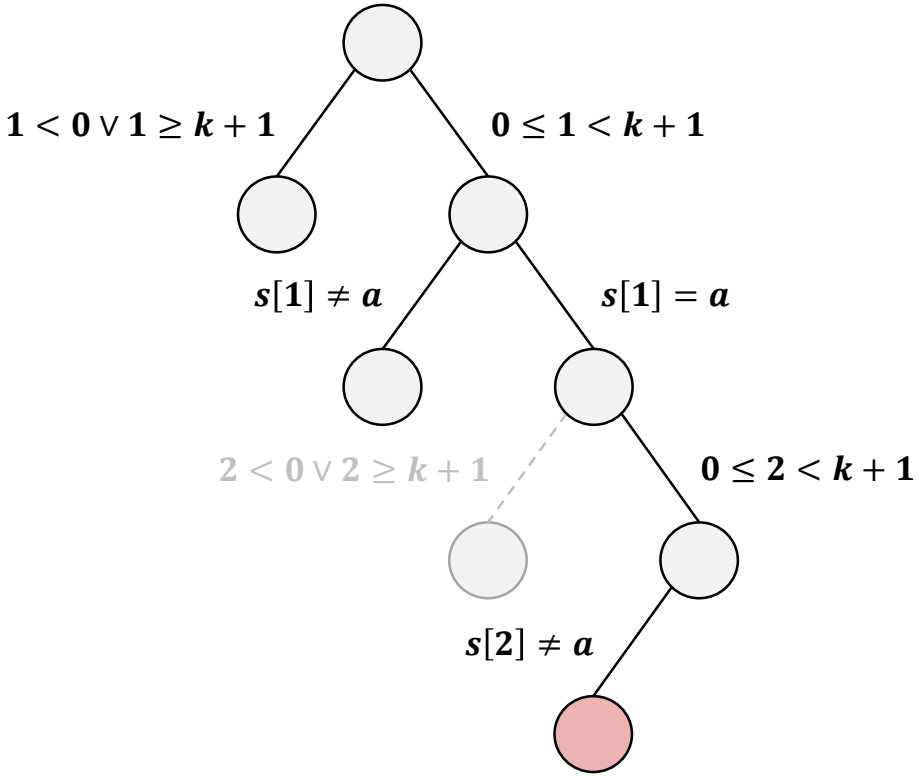
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, 'a');
```



Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

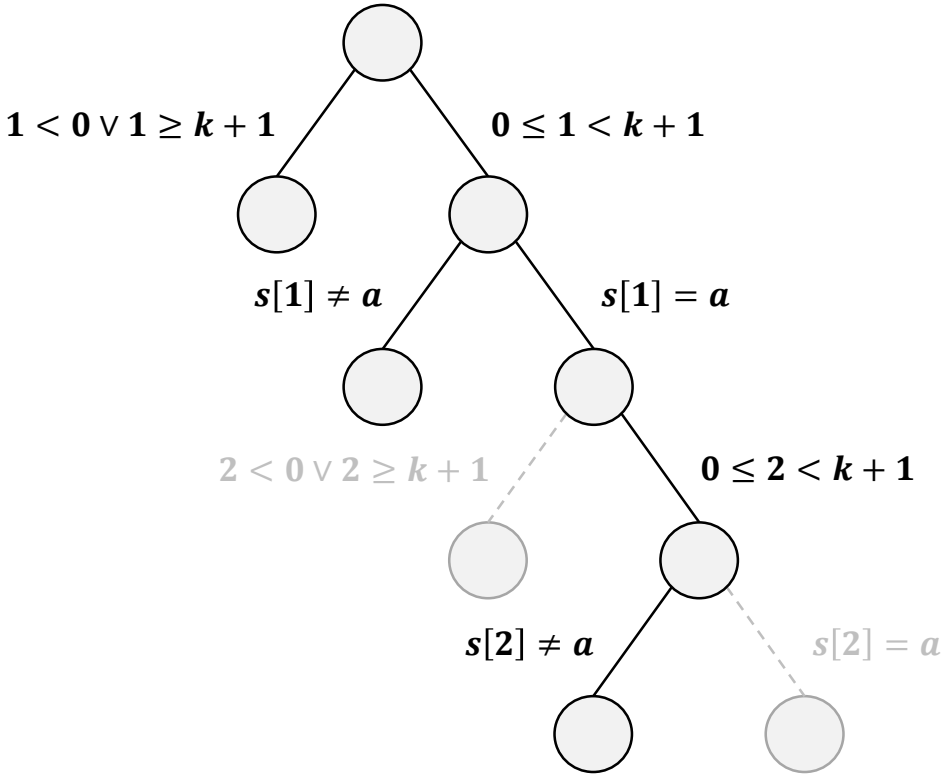
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, 'a');
```



Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, ‘a’);
```

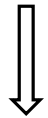


Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s+1, 'a');
```

capacity constraint: $0 < k + 1 \leq 3$



detected bug!

Arising Challenges

- Additional symbolic-size expressions
- Amplifies path explosion
  - Especially with **size-dependent loops**

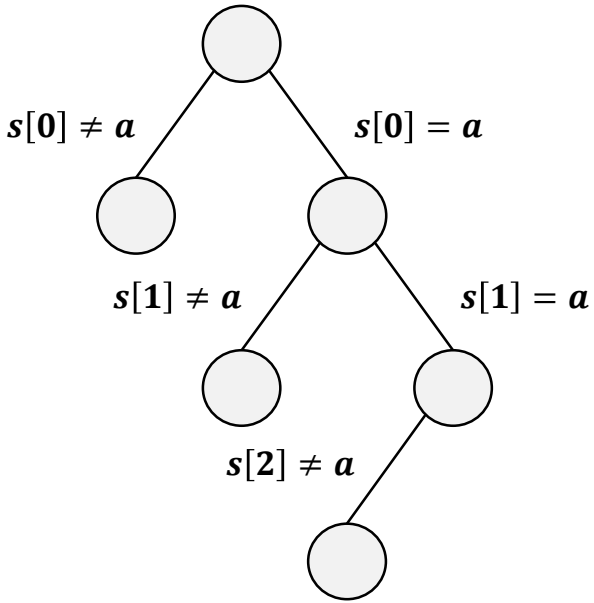
State Merging Approach

- Detect **symbolic-size dependent** loops
- Execute the loop till **full exploration**
- **Merge** the resulting states

State Merging Approach

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, 'a');
```



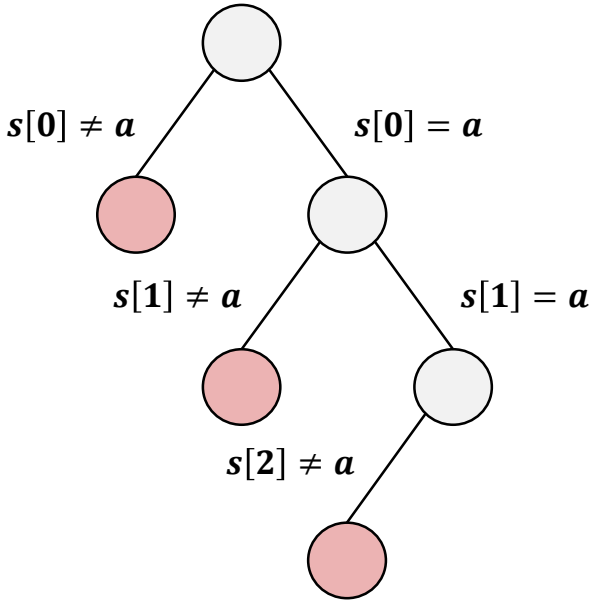
State Merging Approach

merged constraint

$$(0 < k + 1 \leq 3) \ \wedge \ \begin{array}{l}(s[0] \neq a) \vee \\ (s[0] = a \wedge s[1] \neq a) \vee \\ (s[0] = a \wedge s[1] = a \wedge s[2] \neq a)\end{array}$$

prefix

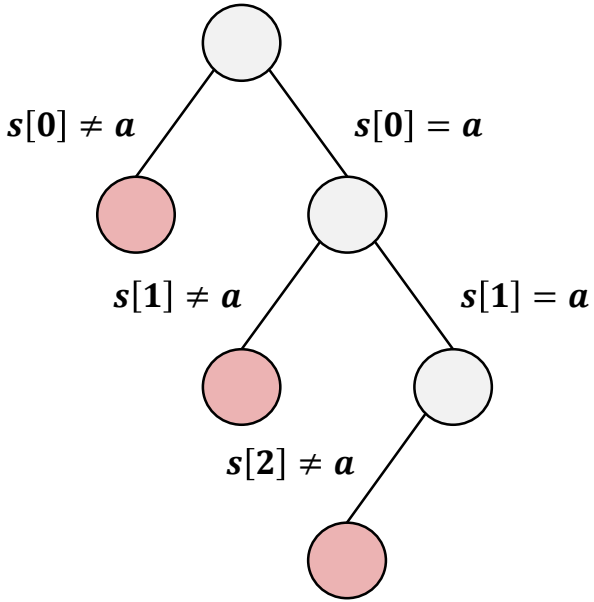
suffix



State Merging Approach

merged constraint

$$(0 < k + 1 \leq 3) \ \wedge \ \begin{pmatrix} (s[0] \neq a) \vee \\ (s[0] = a \wedge s[1] \neq a) \vee \\ (s[0] = a \wedge s[1] = a \wedge s[2] \neq a) \end{pmatrix}$$

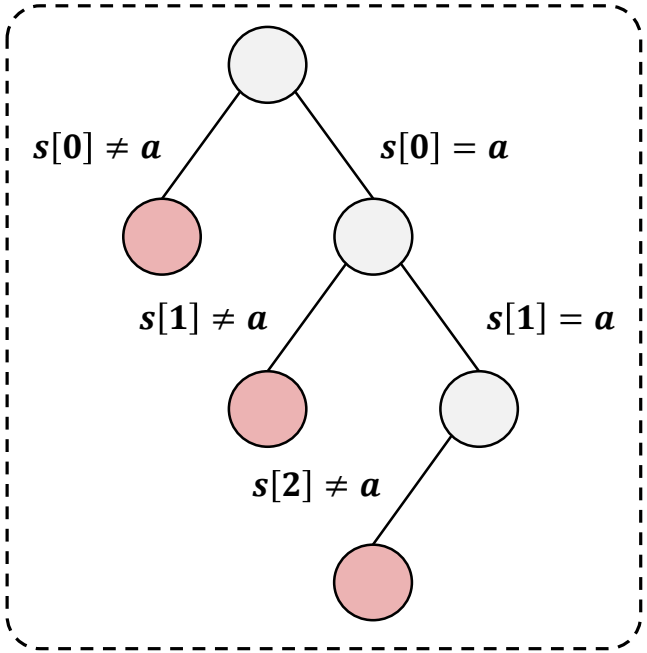


State Merging Approach

merged constraint

$$(0 < k + 1 \leq 3) \ \wedge \ \begin{array}{l}(s[0] \neq a) \vee \\ (s[0] = a \wedge s[1] \neq a) \vee \\ (s[0] = a \wedge s[1] = a \wedge s[2] \neq a)\end{array}$$

⇩ rewrite



$$(0 < k + 1 \leq 3 \wedge (s[0] \neq a \vee (s[0] = a \wedge (s[1] \neq a \vee (s[1] = a \wedge s[2] \neq a)))))$$

Evaluation

Implemented on top of *KLEE*

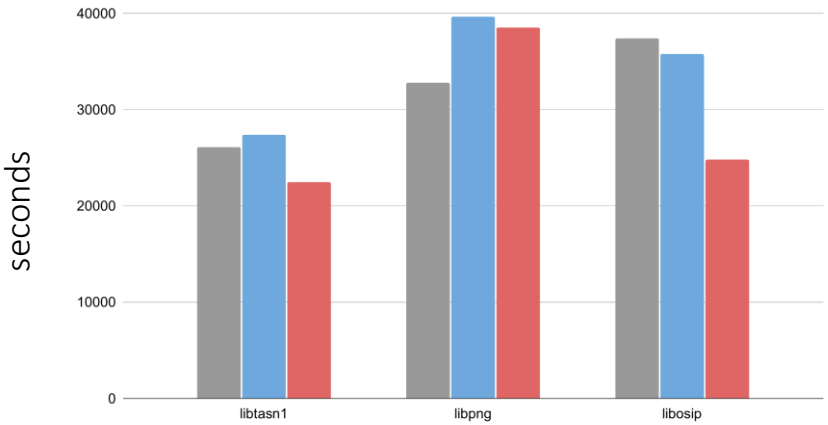
Benchmarks:

- libtasn1, libpng, libosip

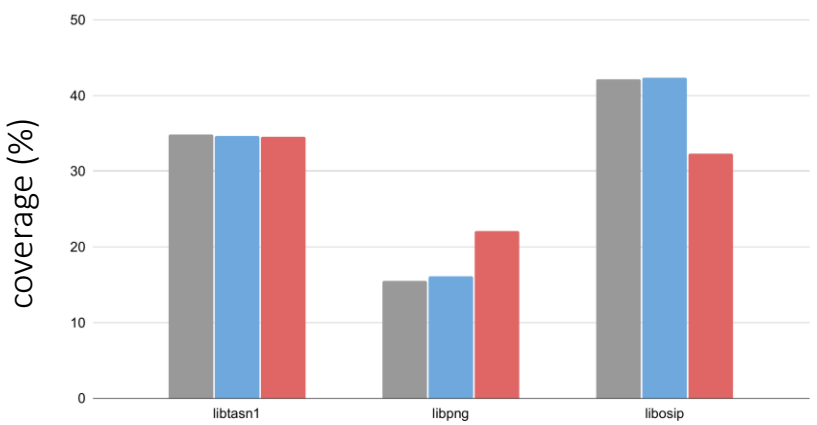
- Concrete size
- Symbolic size (forking)
- Symbolic size (merging)

} modes

Total analysis time



Total coverage



Evaluation

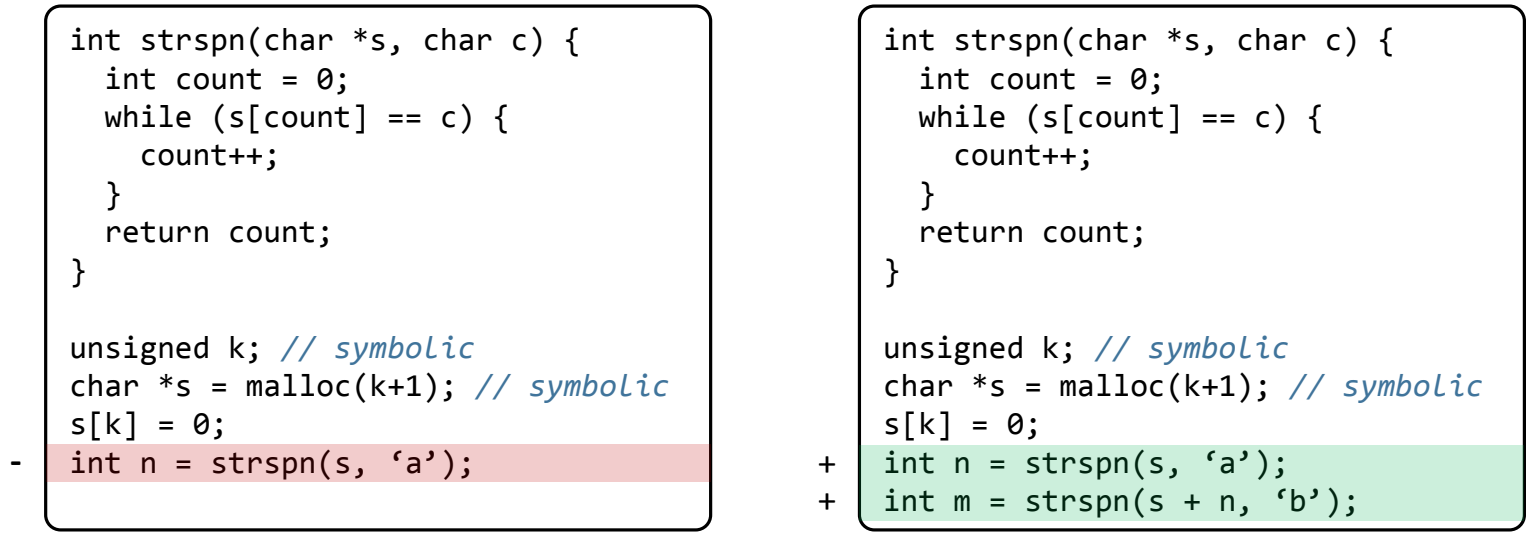
Found bugs:

- libtasn1
  - one *out-of-bound-read*
- oSIP
  - three *out-of-bound-read's*
  - one *integer-underflow*

All the bugs were **confirmed** and **fixed**.

Outline

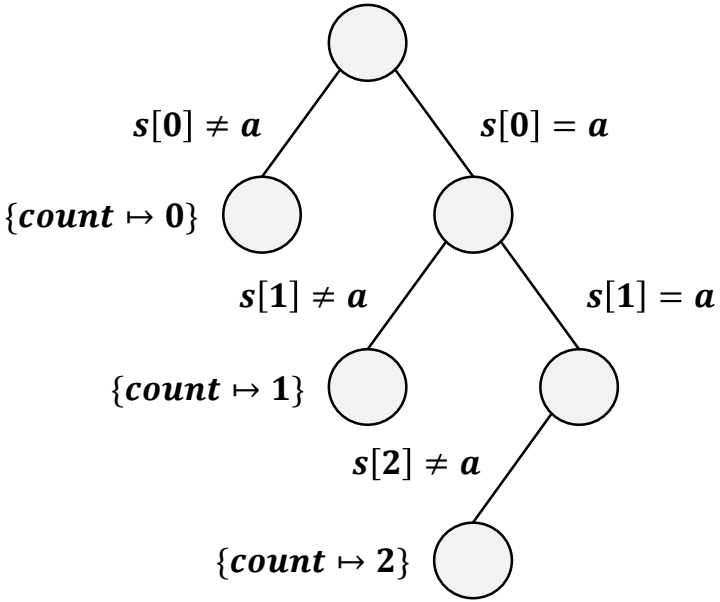
- Background
  - Symbolic execution
  - Memory model
- Symbolic base addresses
  - Relocatable memory model
  - Address-aware query caching
- Symbolic-size allocations
  - Bounded symbolic-size model
  - State merging with quantifiers
- Conclusions and future work


int strspn(char *s, char c) {
int count = 0;
while (s[count] == c) {
count++;
}
return count;
}
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
- int n = strspn(s, ‘a’);
int strspn(char *s, char c) {
int count = 0;
while (s[count] == c) {
count++;
}
return count;
}
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
+ int n = strspn(s, ‘a’);
+ int m = strspn(s + n, ‘b’);

Standard State Merging

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

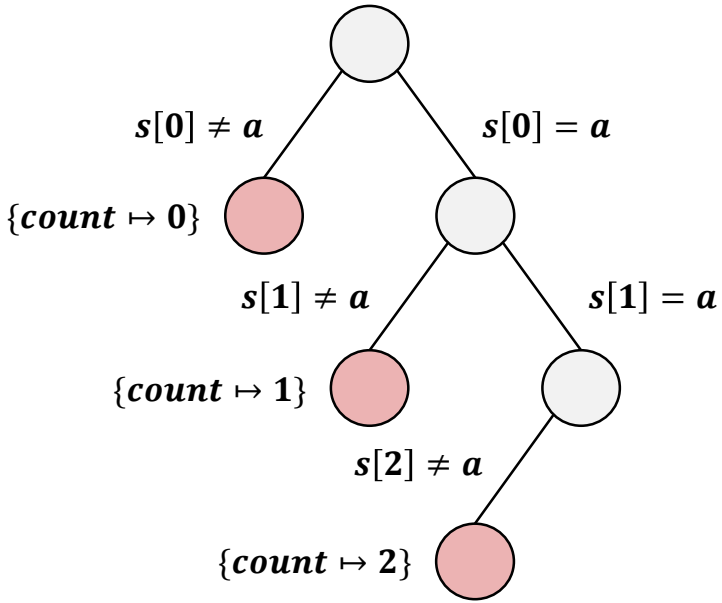
unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, 'a');
int m = strspn(s + n, 'b');
```



Standard State Merging

Merging the **path constraints**

$(s[0] \neq a) \vee$
$(s[0] = a \wedge s[1] \neq a) \vee$
$(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)$

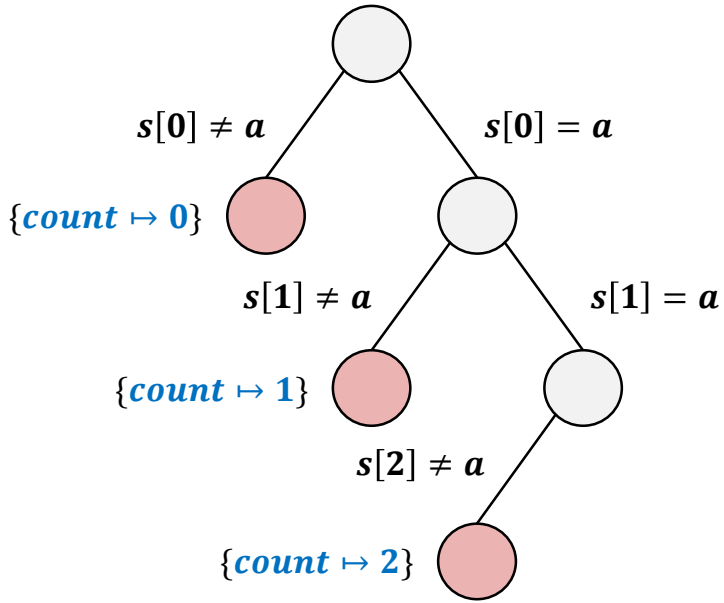


Standard State Merging

Merging the **memory**

$$
\underbrace{\begin{array}{l}
ite(\\
\quad s[0] \neq a, \\
\quad 0, \\
\quad ite(\\
\quad\quad s[0] = a \wedge s[1] \neq a, \\
\quad\quad 1, \\
\quad\quad 2 \\
\quad) \\
)
\end{array}}
$$

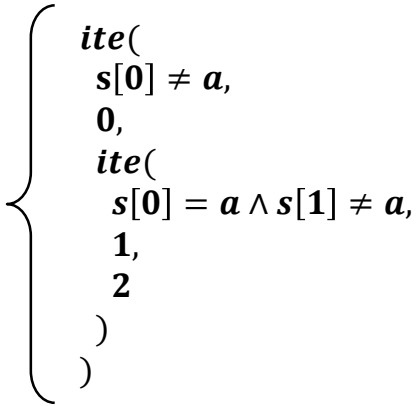
merged value of count



Standard State Merging

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, 'a');
int m = strspn(s + n, 'b');
```



Standard State Merging

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, 'a');
int m = strspn(s + n, 'b');
```

$$
\begin{cases}
ite(\\
\quad s[0] \neq a,\\
\quad 0,\\
\quad ite(\\
\quad\quad s[0] = a \wedge s[1] \neq a,\\
\quad\quad 1,\\
\quad\quad 2\\
\quad)\\
)
\end{cases}
$$

Standard State Merging

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, 'a');
int m = strspn(s + n, 'b');
```

$$
\begin{cases}
ite(\\
\quad s[0] \neq a,\\
\quad 0,\\
\quad ite(\\
\quad\quad s[0] = a \wedge s[1] \neq a,\\
\quad\quad 1,\\
\quad\quad 2\\
\quad)\\
)
\end{cases}
$$

Standard State Merging

Path constraints

$$
\begin{aligned}
&\ldots \wedge \\
&(s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 0] \neq a) \vee \\
&(s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 0] = a \wedge s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 1] \neq a) \vee \\
&(s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 0] = a \wedge s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 1] = a \wedge s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 2] \neq a)
\end{aligned}
$$

Value of **m**

$$
\begin{aligned}
&ite(\\
&\ \mathrm{s}[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 0] \neq a, \\
&\ 0, \\
&\ ite(\\
&\ \ s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 0] = a \wedge s[ite(\mathrm{s}[0] \neq a, 0, ite(s[0] = a \wedge s[1] \neq a, 1,2)) + 1] \neq a, \\
&\ \ 1, \\
&\ \ 2 \\
&\ ) \\
&)
\end{aligned}
$$

State Merging with Quantifiers

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, ‘a’);
int m = strspn(s + n, ‘b’);
```

State Merging with Quantifiers

Merging the path constraints

$$(s[0] \neq a) \vee$$
$$(s[0] = a \wedge s[1] \neq a) \vee$$
$$(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)$$

State Merging with Quantifiers

Merging the path constraints

$$(s[0] \neq a) \vee$$
$$(s[0] = a \wedge s[1] \neq a) \vee$$
$$(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)$$

$$s[0] = a \wedge \cdots \wedge s[i-1] = a \wedge s[i] \neq a$$

$$\Updownarrow$$

State Merging with Quantifiers

Merging the path constraints

$$(s[0] \neq a) \vee$$
$$(s[0] = a \wedge s[1] \neq a) \vee$$
$$(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)$$

$$s[0] = a \wedge \cdots \wedge s[i-1] = a \wedge s[i] \neq a$$

$$\Updownarrow$$

$$(\forall x.\, 1 \leq x \leq i \rightarrow s[x-1] = a) \wedge s[i] \neq a$$

bound variable

State Merging with Quantifiers

Merging the path constraints

$$
\begin{aligned}
&(s[0] \neq a) \vee \\
&(s[0] = a \wedge s[1] \neq a) \vee \\
&(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)
\end{aligned}
$$

$$\Updownarrow$$

$$
\begin{aligned}
&\big((\forall x.\, 1 \leq x \leq 0 \rightarrow s[x-1] = a) \wedge s[0] \neq a\big) \vee \\
&\big((\forall x.\, 1 \leq x \leq 1 \rightarrow s[x-1] = a) \wedge s[1] \neq a\big) \vee \\
&\big((\forall x.\, 1 \leq x \leq 2 \rightarrow s[x-1] = a) \wedge s[2] \neq a\big)
\end{aligned}
$$

State Merging with Quantifiers

Merging the path constraints

$$
\begin{aligned}
&(s[0] \neq a) \vee \\
&(s[0] = a \wedge s[1] \neq a) \vee \\
&(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)
\end{aligned}
$$

$$\Updownarrow$$

$$
\begin{aligned}
&\big((\forall x.\, 1 \leq x \leq 0 \rightarrow s[x-1] = a) \wedge s[0] \neq a\big) \vee \\
&\big((\forall x.\, 1 \leq x \leq 1 \rightarrow s[x-1] = a) \wedge s[1] \neq a\big) \vee \\
&\big((\forall x.\, 1 \leq x \leq 2 \rightarrow s[x-1] = a) \wedge s[2] \neq a\big)
\end{aligned}
$$

$$\Updownarrow$$

$$0 \leq i \leq 2 \wedge (\forall x.\, 1 \leq x \leq i \rightarrow s[x-1] = a) \wedge s[i] \neq a$$

fresh free variable

State Merging with Quantifiers

Merging memory

$0 \leq i \leq 2 \wedge (\forall x. 1 \leq x \leq i \rightarrow s[x-1] = a) \wedge s[i] \neq a$

merged value of **n**

$$
\begin{cases}
ite(\\
\quad s[0] \neq a, \\
\quad 0, \\
\quad ite(\\
\quad\quad s[0] = a \wedge s[1] \neq a, \\
\quad\quad 1, \\
\quad\quad 2 \\
\quad) \\
)
\end{cases}
$$

State Merging with Quantifiers

Merging memory

$0 \leq i \leq 2 \wedge (\forall x. 1 \leq x \leq i \rightarrow s[x-1] = a) \wedge s[i] \neq a$

merged value of **n**

$$
\begin{cases}
ite(\\
\quad s[0] \neq a, \\
\quad 0, \\
\quad ite(\\
\quad\quad s[0] = a \wedge s[1] \neq a, \\
\quad\quad 1, \\
\quad\quad 2 \\
\quad) \\
)
\end{cases}
\quad \Rightarrow \quad i
$$

State Merging with Quantifiers

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, ‘a’);
int m = strspn(s + n, ‘b’);
```

State Merging with Quantifiers

Path constraints

$\ldots \wedge 0 \leq j \leq 2 \wedge (\forall x. 1 \leq x \leq j \rightarrow s[i + x - 1] = b) \wedge s[i + j] \neq b$

Value of **m**

j

Synthesizing Quantified Constraints

path constrains

$(s[0] \neq a)$
$(s[0] = a \wedge s[1] \neq a)$
$(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)$

abstraction

β
$\alpha\beta$
$\alpha\alpha\beta$

$\alpha^0\beta$
$\alpha^1\beta$
$\alpha^2\beta$

$\alpha^*\beta$

quantified path constraints

$0 \leq i \leq 2 \wedge (\forall x.\, 1 \leq x \leq i \rightarrow \varphi_\alpha[x]) \wedge \varphi_\beta[i]$

synthesis constraints

$\varphi_\alpha(1) \stackrel{\text{def}}{=} s[0] = a$
$\varphi_\alpha(2) \stackrel{\text{def}}{=} s[1] = a$

$\varphi_\alpha(x) \stackrel{\text{def}}{=} s[x-1] = a$

$\varphi_\beta(0) \stackrel{\text{def}}{=} s[0] \neq a$
$\varphi_\beta(1) \stackrel{\text{def}}{=} s[1] \neq a$
$\varphi_\beta(2) \stackrel{\text{def}}{=} s[2] \neq a$

$\varphi_\beta(x) \stackrel{\text{def}}{=} s[x] \neq a$

Synthesizing Quantified Constraints

path constrains

$(s[0] \neq a)$
$(s[0] = a \wedge s[1] \neq a)$
$(s[0] = a \wedge s[1] = a \wedge s[2] \neq a)$

$\Longleftrightarrow$

quantified path constraints

$0 \leq i \leq 2 \wedge (\forall x.\, 1 \leq x \leq i \rightarrow \varphi_\alpha[x]) \wedge \varphi_\beta[i]$

abstraction

β
$\alpha\beta$
$\alpha\alpha\beta$

$\alpha^0\beta$
$\alpha^1\beta$
$\alpha^2\beta$

$\} \; \alpha^*\beta$

synthesis constraints

$\varphi_\alpha(1) \stackrel{\text{def}}{=} s[0] = a$
$\varphi_\alpha(2) \stackrel{\text{def}}{=} s[1] = a$

$\Rightarrow \quad \varphi_\alpha(x) \stackrel{\text{def}}{=} s[x-1] = a$

$\varphi_\beta(0) \stackrel{\text{def}}{=} s[0] \neq a$
$\varphi_\beta(1) \stackrel{\text{def}}{=} s[1] \neq a$
$\varphi_\beta(2) \stackrel{\text{def}}{=} s[2] \neq a$

$\Rightarrow \quad \varphi_\beta(x) \stackrel{\text{def}}{=} s[x] \neq a$

Additional Contributions

Specialized solving procedure

- Efficiently solving quantified formulas

Incremental state merging

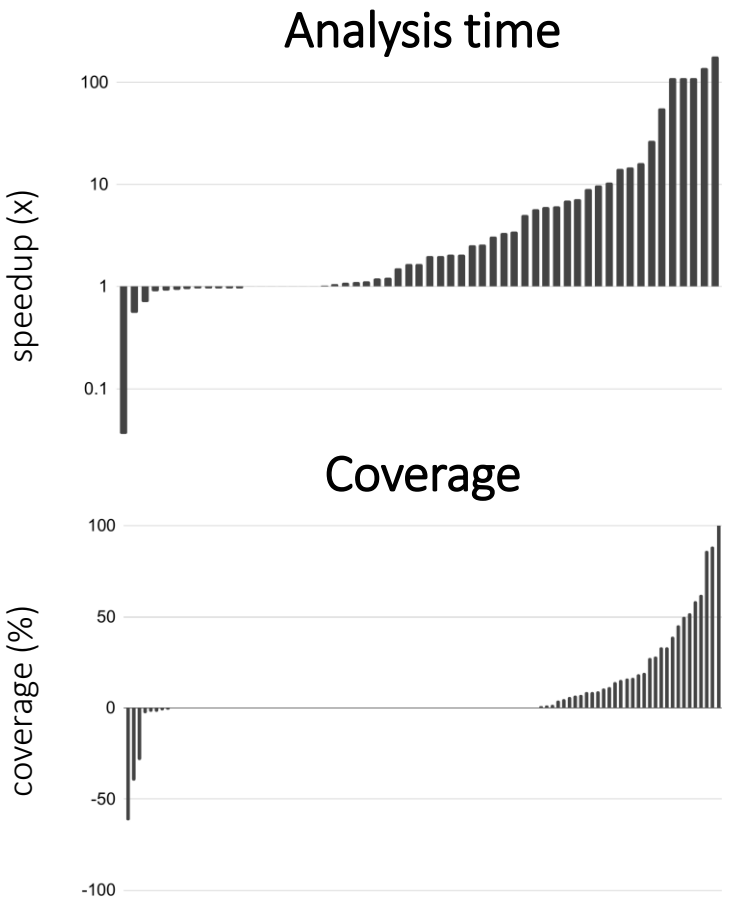
- Handling complex loops (exponential execution trees)

Evaluation

Implemented on top of ***KLEE***

Benchmarks:

- oSIP *(35 subjects)*
- wget *(31 subjects)*
- libtasn1 *(13 subjects)*
- libpng *(12 subjects)*
- apr *(20 subjects)*
- json-c *(5 subjects)*
- busybox *(30 subjects)*



Evaluation

Found bugs in ***klee-uclibc*** in the experiments with ***busybox***

- Two *memory out-of-bound's*

All the bugs were **confirmed** and **fixed**.

Outline

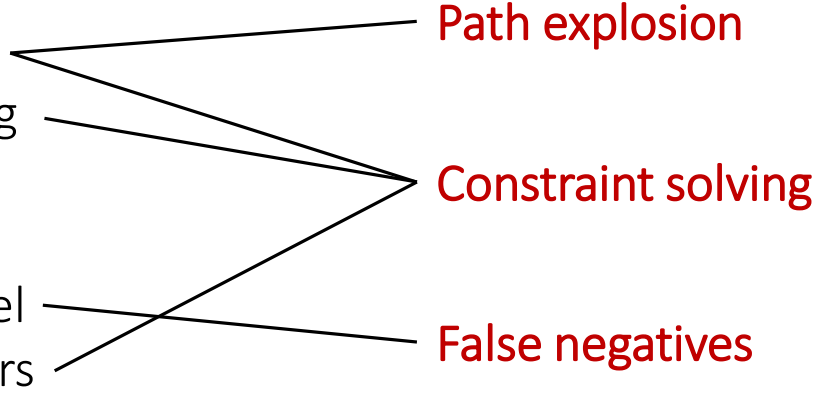
- Background
  - Symbolic execution
  - Memory model
- Symbolic base addresses
  - Relocatable memory model
  - Address-aware query caching
- Symbolic-size allocations
  - Bounded symbolic-size model
  - State merging with quantifiers
- Conclusions and future work

Summary

Tackle the challenges of **symbolic execution** using
novel memory models

Symbolic base addresses:
- Relocatable memory model
- Address-aware query caching

Symbolic-size allocations:
- Bounded symbolic-size model
- State merging with quantifiers



Publications

Past-Sensitive Pointer Analysis for Symbolic Execution **(FSE 2020)**

- *D. Trabish, T. Kapus, N. Rinetzky, C. Cadar*

Relocatable Addressing Model for Symbolic Execution **(ISSTA 2020)**

- *D. Trabish, N. Rinetzky*

Address-Aware Query Caching for Symbolic Execution **(ICST 2021)**

- *D. Trabish, S. Itzhaky, N. Rinetzky*

A Bounded Symbolic-Size Model for Symbolic Execution **(FSE 2021)**

- *D. Trabish, S. Itzhaky, N. Rinetzky*

State Merging with Quantifiers in Symbolic Execution **(FSE 2023)**

- *D. Trabish, N. Rinetzky, S. Shoham, V. Sharma*

Implementations

Past-Sensitive Pointer Analysis

- *https://github.com/davidtr1037/klee-pspa*

Relocatable Memory Model

- *https://github.com/davidtr1037/klee-ram*

Address-Aware Query Caching

- *https://github.com/davidtr1037/klee-aaqc*

Bounded Symbolic-Size Model

- *https://github.com/davidtr1037/klee-symsize*

State Merging with Quantifiers

- *https://github.com/davidtr1037/klee-quantifiers*

Future Work

- Generalizing the relocatable memory model
- Modeling unbounded objects
- More applications with quantified encoding
- Generalizing the solving procedure for quantified constraints

Thanks!

Backup

Publications

TODO

Symbolic State

TODO

Logic / SMT Theories

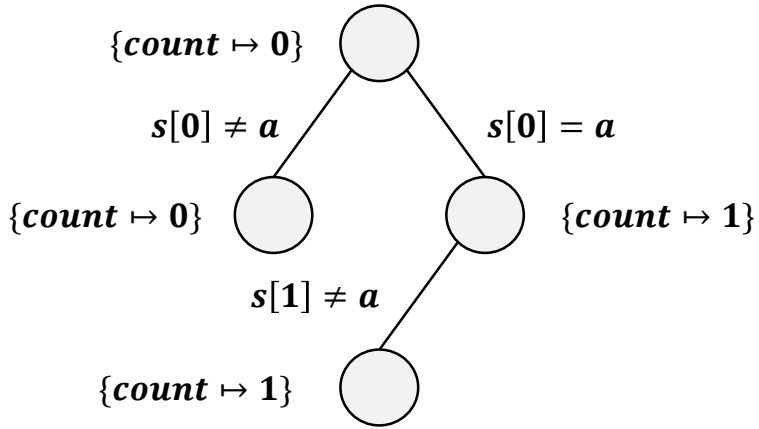
TODO

Example

```
int strspn(char *s, char c) {
  int count = 0;
  while (s[count] == c) {
    count++;
  }
  return count;
}

unsigned k; // symbolic
char *s = malloc(k+1); // symbolic
s[k] = 0;
int n = strspn(s, 'a');
if (n > 1) {
  // do something...
}
```

UNSAT



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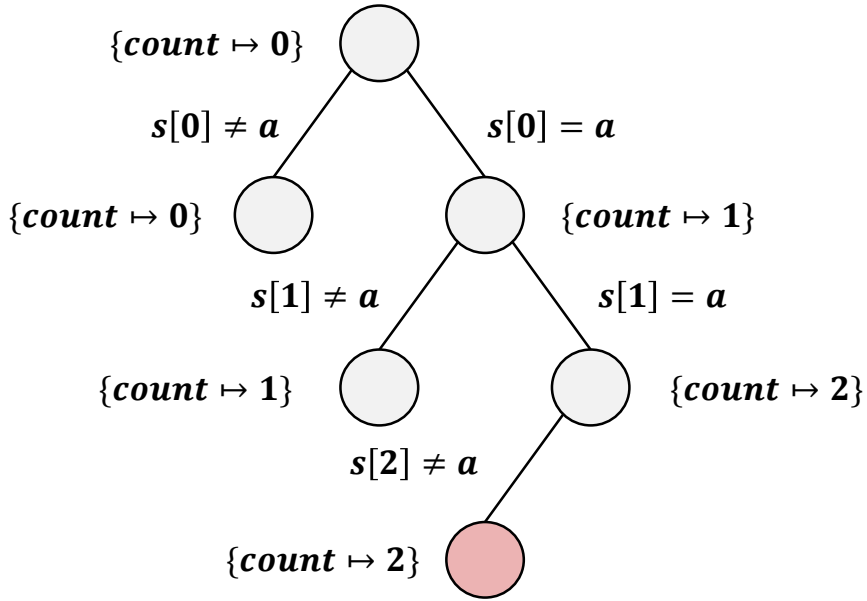


Symbolic Pointers

```
char **array = calloc(3, PTR_SIZE);
for (int i = 0; i < 3; i++) {
  array[i] = calloc(10, 1);
}

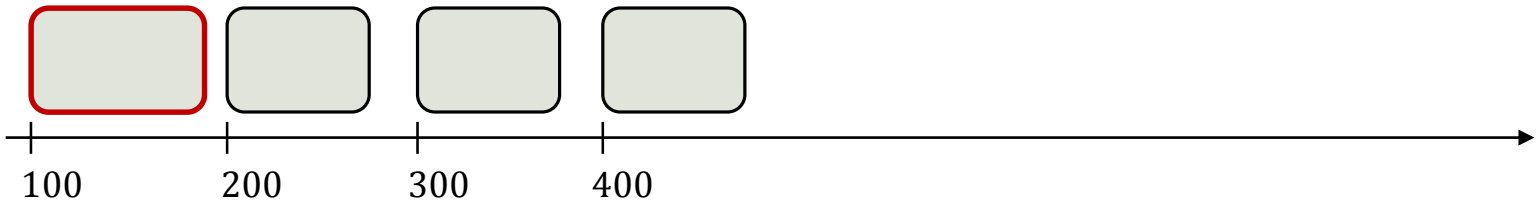
// symbolic: i < 2, j < 10
unsigned i, j;
if (array[i][j] == 7) {
  // ...
}
```

$$p \stackrel{\text{def}}{=} select(a[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i) + j$$

resolution query

$$i < 2 \ \wedge \ j < 10 \ \wedge \ 100 \leq p < 116$$

UNSAT



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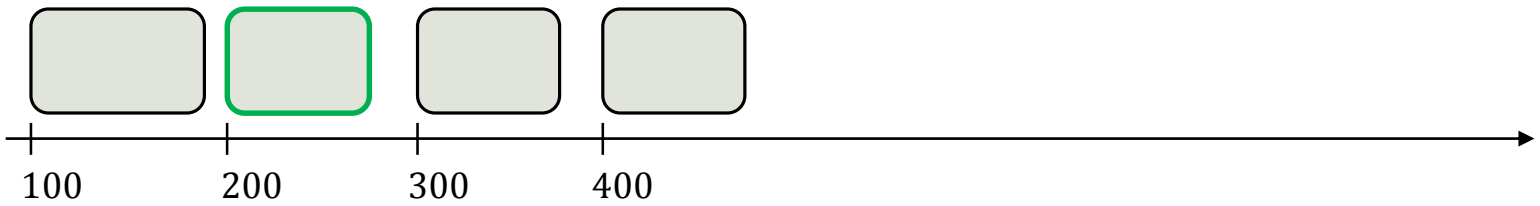
resolution query

$$i < 2 \ \wedge \ j < 10 \ \wedge \ 200 \leq p < 210$$

SAT



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}
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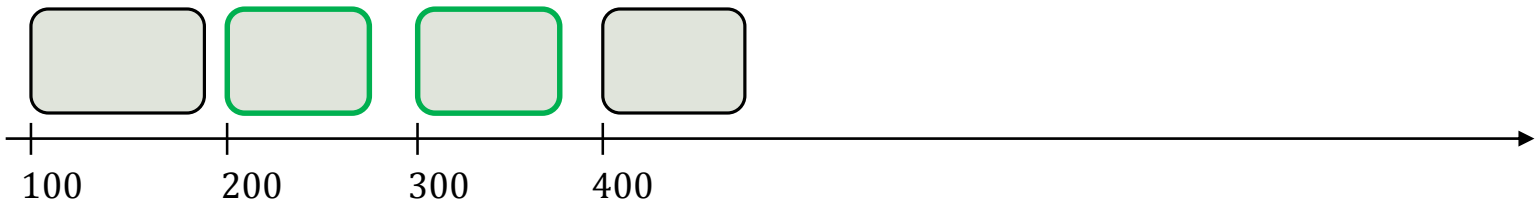
resolution query

$$i < 2 \ \wedge \ j < 10 \ \wedge \ 300 \leq p < 310$$

SAT



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```

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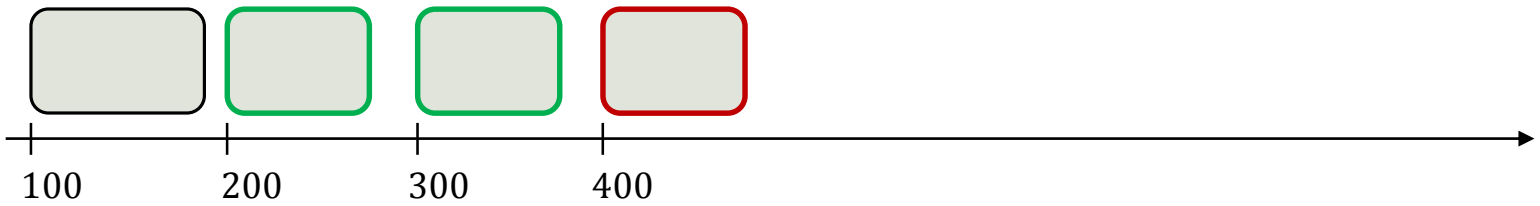
resolution query

$$i < 2 \ \wedge \ j < 10 \ \wedge \ 400 \leq p < 410$$

UNSAT



Address-Dependent Queries

$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i) + j$

$pc \stackrel{\text{def}}{=} z \neq 0 \ \wedge \ i < 2 \ \wedge \ j < 10 \ \wedge \ 200 \leq p < 210$

query:

$pc \ \wedge \ select(a_2, p - 200) = 7$

$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 600, 1 \mapsto 700, 2 \mapsto 800], i) + j$

$pc \stackrel{\text{def}}{=} z = 0 \ \wedge \ i < 2 \ \wedge \ j < 10 \ \wedge \ 600 \leq p < 610$

query:

$pc \ \wedge \ select(a_2, p - 600) = 7$

Address-Dependent Queries

$p \stackrel{\text{def}}{=} select(a_1[0 \mapsto 200, 1 \mapsto 300, 2 \mapsto 400], i) + j$

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query:

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$pc \stackrel{\text{def}}{=} i < 2 \wedge j < 10 \wedge 600 \leq p < 610$

query:

$pc \wedge select(a_2, p - 600) = 7$

- Equisatisfiable
- Query caching **fails** (No common normal form)